



Product Manual 26179 (Revision J, 7/2017) Original Instructions



AtlasSC[™] Digital Control

Control Part Numbers 8273-040, -041, -042, -043

Installation and Operation Manual

Read this entire manual and all other publications pertaining to the work to be performed before installing, operating, or servicing this equipment.



General Precautions Practice all plant and safety instructions and precautions.

Failure to follow instructions can cause personal injury and/or property damage.



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Any unauthorized modifications to or use of this equipment outside its specified mechanical, electrical, or other operating limits may cause personal injury and/or property damage, including damage to the equipment. Any such unauthorized modifications: (i) constitute "misuse" and/or "negligence" within the meaning of the product warranty thereby excluding warranty coverage for any resulting damage, and (ii) invalidate product certifications or listings.



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Warnings and Notices

Important Definitions



This is the safety alert symbol used to alert you to potential personal injury hazards. Obey all safety messages that follow this symbol to avoid possible injury or death.

- DANGER Indicates a hazardous situation, which if not avoided, will result in death or serious injury.
- **WARNING** Indicates a hazardous situation, which if not avoided, could result in death or serious injury.
- **CAUTION** Indicates a hazardous situation, which if not avoided, could result in minor or moderate injury.
- NOTICE Indicates a hazard that could result in property damage only (including damage to the control).
- **IMPORTANT** Designates an operating tip or maintenance suggestion.



Personal Protective Equipment

- The products described in this publication may present risks that could lead to personal injury, loss of life, or property damage. Always wear the appropriate personal protective equipment (PPE) for the job at hand. Equipment that should be considered includes but is not limited to:
- Eye Protection
- Hearing Protection
- Hard Hat
- Gloves
- Safety Boots
- Respirator

Always read the proper Material Safety Data Sheet (MSDS) for any working fluid(s) and comply with recommended safety equipment.



Be prepared to make an emergency shutdown when starting the engine, turbine, or other type of prime mover, to protect against runaway or overspeed with possible personal injury, loss of life, or property damage.

Electrostatic Discharge Awareness

NOTICE	Electronic controls contain static-sensitive parts. Observe the following precautions to prevent damage to these parts:
Electrostatic Precautions	 Discharge body static before handling the control (with power to the control turned off, contact a grounded surface and maintain contact while handling the control). Avoid all plastic, vinyl, and Styrofoam (except antistatic versions) around printed circuit boards. Do not touch the components or conductors on a printed circuit board with your hands or with conductive devices. To prevent damage to electronic components caused by improper handling, read and observe the precautions in Woodward manual 82715, Guide for Handling and Protection of Electronic Controls, Printed Circuit Boards, and Modules.

Follow these precautions when working with or near the control.

- 1. Avoid the build-up of static electricity on your body by not wearing clothing made of synthetic materials. Wear cotton or cotton-blend materials as much as possible because these do not store static electric charges as much as synthetics.
- 2. Do not remove the printed circuit board (PCB) from the control cabinet unless absolutely necessary. If you must remove the PCB from the control cabinet, follow these precautions:
 - Do not touch any part of the PCB except the edges.
 - Do not touch the electrical conductors, the connectors, or the components with conductive devices or with your hands.
 - When replacing a PCB, keep the new PCB in the plastic antistatic protective bag it comes in until you are ready to install it. Immediately after removing the old PCB from the control cabinet, place it in the antistatic protective bag.

Regulatory Compliance

North American Compliance:

These listings are limited to those units bearing the UL agency identification.

Marine Compliance	
American Bureau of Shipping:	ABS Rules 2003 SVR 4-2-1/7.3, 7.5.1, 7.9.3/17, 4-9-4/23, 4-9-7/Table 9
Det Norske Veritas:	Standard for Certification No. 2.4, 2001: Temperature Class B, Humidity Class B, Vibration Class C, and EMC Class A
Lloyd's Register of Shipping:	LR Type Approval Test Specification No. 1, 2002 for Environmental Categories ENV1, ENV2, and ENV3

Special Conditions for Safe Use

The AtlasSC control is suitable for use in Class I, Division 2, Groups A, B, C, D, per UL for Canada and US or non-hazardous locations only.

Wiring must be in accordance with North American Class I, Division 2 wiring methods as applicable, and in accordance with the authority having jurisdiction.

Field wiring must be suitable for at least 75 °C for operating ambient temperatures expected to exceed 50 °C.

A fixed wiring installation is required.

Do not connect more than one main power supply to any one fuse or circuit breaker.

Connect ground screw to earth ground (see Figure 3-1).



EXPLOSION HAZARD—Do not connect or disconnect while circuit is live unless area is known to be non-hazardous.

Substitution of components may impair suitability for Class I, Division 2 applications.



RISQUE D'EXPLOSION—Ne pas raccorder ni débrancher tant que l'installation est sous tension, sauf en cas l'ambiance est décidément non dangereuse.

La substitution de composants peut rendre ce matériel inacceptable pour les emplacements de Classe I, Division 2.

UL: UL Listed for Class I, Division 2, Groups A, B, C, & D, T4A at 70 °C ambient. For use in Canada and the United States.

11	MPORTANT	Some degraded performance can occur on RTD and thermocouple inputs in the presence of radiated energy from transmitters such as cell phones. This degraded performance is in the form of a slight change in the accuracy of the measured temperature. It is recommended that operation of such devices be kept more than 3 m from the AtlasSC control. This will prevent any degradation. Installation of the unit in a metal enclosure will also prevent

Chapter 1. General Information

Introduction

This manual describes the Woodward AtlasSC[™] Digital Control. It provides a variety of useful information for the user ranging from simple basic descriptions to detailed information on wiring, specifications, programming, and functionality. Included are:

- General information on the AtlasSC platform and available versions
- A physical description of the control hardware
- A description of all AtlasSC modules
- A listing of accessories that may be used with the platform
- Information on AtlasSC programming, distributed I/O (input/output), and control communications
- Installation and maintenance
- Troubleshooting information

AtlasSC Control Description

The AtlasSC digital control platform fits a wide range of prime mover applications. These include small mechanical-drive units with a minimum of complexity on up to small two-shaft gas turbine generator sets that require unit sequencing and load control. The AtlasSC control is programmed to the specific needs of the prime mover and its driven load.

At the heart of the AtlasSC control is a 32-bit microprocessor that runs a powerful Real Time Operating System. This operating system is specifically designed to control the proper timing of all application code so that dynamic performance of the final control system is absolutely guaranteed. Each piece of the application code is "scheduled" under a Rate Group structure that ensures execution of the code at a predetermined time.

Application programming is accomplished via Woodward's GAP[™] Graphical Application Program. GAP is a pictures-to-code system that provides a high-level programming environment for users who have control expertise but do not have specific programming skills. Once the application program has been generated and loaded into the AtlasSC control, the user can view variables and tune the control with the Woodward Watch Window Standard service tool. Connection to other devices, such as an HMI, is accomplished by means of serial Modbus[®]* ports on the control, and the desired information flow is programmed into the control via GAP. Another serial Modbus port can be used to connect distributed I/O if required. *—Modbus is a trademark of Schneider Automation Inc.

The hardware platform is based on the industry-standard PC/104 bus structure. In the AtlasSC control, the backplane is the SmartCore board. The PC/104 modules are "stacked" onto the SmartCore board in order to add I/O or other functionality. Each of the stacked modules has an on-board DIP switch that is positioned to the unique address of that particular module. The AtlasSC control uses a second stack called the Power Bus Stack. This stack is used primarily for power-related I/O. Three serial Modbus communications ports are available for a variety of uses. The control runs on low-voltage dc power (18–32 Vdc). AtlasSC field wiring is accomplished via terminal blocks that plug into the control modules.

Control Versions

The AtlasSC control versions are differentiated by anticipated application categories. As such, the I/O content will vary, and one or more of the optional I/O modules may be required. Table 1-1 shows the configuration of the modules for the AtlasSC part numbers that are available.

Part Number	Application Category	Smart Core	Power Supply	Analog Combo	Power Sense
8273-040	Small gen set or mechanical drive	Yes	Yes	No	No
8273-041	Large gen set or mechanical drive	Yes	Yes	Yes	No
8273-042	Small gen set with real power sensing	Yes	Yes	No	Yes
8273-043	Large gen set with real power sensing	Yes	Yes	Yes	Yes

Table 1-1. AtlasSC Versions

Figure 1-1 shows diagrammatically the physical arrangement of the modules in the AtlasSC hardware. The hardware uses two stacked-bus arrangements to provide the required structure. The Power Bus Stack is used for the power-related I/O as well as the discrete output drivers. The PC/104 Stack is used primarily for the signal I/O. Depending on the part number specified, the AtlasSC control may or may not have an Analog Combo board or a PowerSense board (refer to Table 1-1 above). The stack arrangement shown in Figure 1-1 is for the maximum configuration (8273-043).





Control Accessories

The AtlasSC digital control is designed to interface with the Woodward Watch Window Standard and Watch Window Professional service tools. Watch Window is a configuration and troubleshooting tool that provides a window into the control system. It runs on a PC and is connected to a serial communication port on the AtlasSC control. Watch Window is available from the Woodward website (www.woodward.com/ic) or from a CD-ROM that can be purchased.

Watch Window Standard performs three primary functions:

- Initial Control Configuration
- Configures the AtlasSC control to specific application or site requirements.
- Monitoring and Tuning
- Allows the user to view system variables and tune the application software for desired performance.
 Management of Configuration and Setpoints
- Allows the user to upload or download all tuning and configuration variables to or from the AtlasSC control.

Watch Window Professional provides all of the functions of Watch Window Standard, plus it provides the tools to download application code into the AtlasSC hardware.

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Figure 1-2. AtlasSC Control Layout







BOTTOM VIEW

261-076 02-7-8

Figure 1-3. AtlasSC Control (8273-040)



AtlasSC[™] Digital Control







BOTTOM VIEW











Figure 1-5. AtlasSC Control (8273-042)



AtlasSC[™] Digital Control







BOTTOM VIEW

261-074 02-7-8

Figure 1-6. AtlasSC Control (8273-043)

Chapter 2 Installation

Introduction

This chapter provides the general information for mounting location selection, installation, and wiring of the AtlasSC[™] control. Hardware dimensions, ratings, and requirements are given for mounting and wiring the control in a specific application.

General Installation

When selecting a location for mounting the AtlasSC control, consider the following:

- Protect the unit from direct exposure to water or to a condensation-prone environment.
- The operating range of the AtlasSC control is –20 to +70 °C (–4 to +158 °F).
- Provide adequate ventilation for cooling. Shield the unit from radiant heat sources.
- Do not install near high-voltage or high-current devices.
- Allow adequate space around the unit for servicing and wiring.
- Do not install where objects can be dropped on the terminals.
- Ground the chassis for proper safety and shielding.
- When installing on a generator set package, provide vibration isolation.

Shipping Carton

Before unpacking the control, refer to the inside front cover and page vi of this manual for WARNINGS and NOTICES. Be careful when unpacking the control. Check for signs of damage such as bent or dented panels, scratches, loose or broken parts. If any damage is found, immediately notify the shipper.

The AtlasSC control was shipped from the factory in an anti-static foam lined carton. This carton should always be used for transport of the control when it is not installed.

Mounting

Figure 1-2 shows the AtlasSC control layout and mounting pattern. The AtlasSC digital control is to be mounted in an appropriate enclosure for the installed environment. This equipment is designed for installation within a control room panel or cabinet.

The standard AtlasSC package must be mounted to allow sufficient room for wiring access. Eight front panel mounting holes permit secure mounting. Depending on its configuration, the AtlasSC weighs between 3.4 and 4.5 kg (7.5 and 10 pounds), and has an operating range of –20 to +70 °C ambient air temperature. A minimum of 25 mm (1 inch) of clear space around the outer surfaces of the AtlasSC is adequate for ventilation, however approximately 75 mm (3 inches) of space may be required for wiring, depending on wire size.

Environmental Specifications

Operating Temperature

The AtlasSC Control Platform operates in a specified ambient temperature of -20 to +70 °C (-4 to +158 °F).



Continuous operation with insufficient airflow or higher operating temperatures will lead to reduced reliability and possible damage to the control.

Storage Temperature

The AtlasSC Control Platform is designed to be stored without applied power at the temperature range of -40 to +105 °C (-40 to +221 °F).

Shock

The AtlasSC Control Platform was designed to meet the shock requirements specified by MIL-STD-810C procedure 516.2, procedure 1 (30 Gs, 11 millisecond half sine pulse). During Shock, relay bounce shall be limited to less than 100 ms.

Vibration (Random)

SAE J1455 (10-2000 Hz @ 0.04 G²/Hz, 8.2 Grms PSD)

Vibration (Sine)

Lloyds Register Test Specification No. 1 Vibration Test 1 (5-13.2 Hz, ±1 mm; 13.2-100 Hz, 0.7 g)

Audible Noise Emission

The AtlasSC Control Platform does not emit an audible noise above 70 dBA as measured 1 meter away, with or without a fan.

Electrical Connections

Most inputs and outputs to the AtlasSC control are made through "CageClamp" terminal blocks. For noise suppression, it is recommend that all low-current wires be separated from all highcurrent wire.

The pluggable terminal blocks on the I/O modules are screwless CageClamp style blocks. The spring clamp can be actuated by using a standard 2.5 mm (3/32 inch) flat bladed screwdriver (see Figure 2-1). The AtlasSC pluggable terminal blocks accept wires from 0.08–1.1 mm² (28–18 AWG). Two 0.5 mm² (20 AWG) wires or three 0.3 mm² (22 AWG) wires can be easily installed in each terminal. Wires for the pluggable I/O terminals should be stripped 8 mm (0.3 inch).

Most of the AtlasSC control's terminal blocks are designed to be removed by hand. After AtlasSC input power is disconnected, the terminal blocks can be removed one at a time by pulling them straight out. Be careful not to pull the plug out at an angle, as this will fracture the end terminal.



Method #1 Free Hand (holds spring open)

Method #2 Bench (momentarily opens spring while force is applied)

Figure 2-1. Spring Clamp

The AtlasSC fixed terminal blocks used for the power supply input accept wires from 0.08–1.1 mm² (28– 18 AWG). Two 0.5 mm² (20 AWG) wires or three 0.3 mm² (22 AWG) wires can be easily installed in each terminal. Wires for the fixed mounted power terminals should be stripped 5 mm (0.2 inch).



Figure 2-2. Wiring Fixed Terminal



All ac wiring for voltages and currents is done with fixed screw barrier blocks rather than pluggable terminal blocks. The fixed screw barrier blocks accept wires terminated into terminal lugs for #6 screws.





Grounding

Protective Earth (PE) must be connected to the termination point on the backside of the unit next to the label with the symbol to reduce the risk or plectric shock. This connection will be made using a thread-forming screw (M4 x 6 mm). The conductor providing the connection shall have a properly sized ring lug and wire larger than or equal to 3.3 mm² (12 AWG).

Recommended Grounding Practices

Providing the proper ground for the AtlasSC control is important. Improper connection of the control chassis to the ground plane may lead to stray currents between the reference point for the ac signal sources (current and voltage transformers), and the reference point for the sensing inputs on the AtlasSC control. Differences in potential between these two points results in equalizing current flow which then produces unacceptably high common mode voltages. Common mode voltages may result in improper readings for the sensed ac inputs, or even damage to the AtlasSC control in extreme cases. To minimize this problem, it is necessary to provide a low resistance path between the ac signal reference point, and the chassis of the AtlasSC control. Typically this point is the designated ground for the generator set and related instrument transformers.

Shields and Grounding

An individual shield termination is provided at the terminal block for each of the signals requiring shielding. These include all sensor wiring and ac signal inputs. Relay outputs, contact inputs, and power supply wiring do not normally require shielding, but can be shielded if desired. All shielded cable must be twisted conductor pairs. Do not attempt to tin (solder) the braided shield. All signal lines should be shielded to prevent picking up stray signals from adjacent equipment. Wire exposed beyond the shield should be as short as possible, not exceeding 50 mm (2 inches). The other end of the shields must be left open and insulated from any other conductor.

The AtlasSC control is designed for shield termination to earth ground at the control. If intervening terminal blocks are used in routing a signal, the shield should be continued through the terminal block. If shield grounding is desired at the terminal block, it should be ac coupled to earth. All other shield terminations except at the AtlasSC control should be ac coupled to earth through a capacitor. A 1000 pF, 500 V capacitor is sufficient. The intent is to provide a low impedance path to earth for the shield at frequencies of 150 kHz and up. Multiple direct connections of a shield to earth risk high levels of current to flow within the shield. See Woodward application note 50532, *Interference Control in Electronic Governing Systems*, for more information.

Where shielded cable is required, cut the cable to the desired length and prepare the cable as instructed below.

- 1. Strip outer insulation from both ends, exposing the braided or spiral wrapped shield. Do not cut the shield.
- 2. Using a sharp, pointed tool, carefully spread the strands of the shield.
- 3. Pull inner conductor(s) out of the shield. If the shield is the braided type, twist it to prevent fraying.
- 4. Remove 6 mm (1/4 inch) of insulation from the inner conductors.
- 5. Connect wiring and shield as shown in plant wiring diagram.

For noise suppression reasons, it is recommend that all low-current wires be separated from all highcurrent wires. Input power ground terminal should also be wired to earth ground. Installations with severe electromagnetic interference (EMI) may require additional shielding precautions, such as wire run in conduit or double shielding. Contact Woodward for more information.

Shields can be grounded at both ends (AtlasSC control and load) if the cable length is sufficiently short (within a cabinet) to prevent ground loop current in the shield.

Cabinet Installations: If the AtlasSC control is installed in a cabinet, shielded I/O can be terminated directly to the cabinet (earth ground) at the entry to the cabinet, as well as at the control.

Input Power

The AtlasSC control requires a nominal voltage source of 18 to 32 Vdc. Input power requirements vary depending on the control version. Table 2-1 contains information for the maximum configuration.

NOTICE	Power must be applied to the AtlasSC control at least 60 seconds prior to expected use. The control must have time to do its power up diagnostics to become operational. Failure of the diagnostics will disable control function.		
	The AtlasSC power supply board must have the input power removed before installing or removing.		
	This equipment is suitable for use in Class 1, Division 2, Groups A, B, C, and D, or non-hazardous locations only.		
	Wiring must be in accordance with Class I, Division 2 wiring methods and in accordance with the authority having jurisdiction.		
	Do not connect more than one main power supply to any one fuse or circuit breaker.		

Power supply output must be of a low impedance type for proper operation of the control. DO NOT power a control from a high voltage source containing dropping resistors and Zener diodes. If batteries are used for operating power, an alternator or other battery-charging device is necessary to maintain a stable supply voltage.

Significant inrush currents are possible when current is applied to the AtlasSC control. The magnitude of the inrush current depends on the power source impedance, so Woodward cannot specify the maximum inrush current. Time-delay fuses or circuit breakers must be used to avoid nuisance trips.

Input

Protect

ust be connected to the chassis at the termination point on the back side

labeled . The power supply grounding terminals should also be connected to earth to ensure grounding of the power supply printed circuit boards. The grounding conductor must be the same size as the main supply conductors or the PT wires, whichever is larger.

Note that the control's power supplies are not equipped with input power switches. For this reason, some means of disconnecting input power to each main power supply must be provided for installation and servicing.

It is expected that the installation of this equipment will include over current protection between the power source and the AtlasSC control. This over current protection may be accomplished by series connection of properly rated fuses or circuit breakers. Branch circuit protection of no more than 250% of the maximum AtlasSC power supply input current rating must be provided. See Table 2-1 for maximum recommended fuse ratings. This value meets the 250% UL listing requirements. The use of properly sized UL class CC, J, T, G, RK1, or RK5 fuses meet the requirements for branch circuit protection. Do not connect more than one AtlasSC control to any one fuse. Use only the wire size specified in Table 2-1 or equivalent metric size which meets local code requirements. Time delay fuses should be used to prevent nuisance trips.

Table 2-1 provides the power supply holdup time specification; which is the time the supply will continue to operate within specification after its input power is interrupted. This information may be useful in specifying uninterruptible power supply (UPS) systems.

Table 2-1. Power Supply Requirements

Input Voltage Range	Fuse (Current Rating)	Fuse (I ² t Rating)	Wire Size* **	Holdup Time
18–32 Vdc**	9 A	>800	2/4 mm² 12/14 AWG	8 ms

IMPORTANT	* 4 mm ² (12 AWG) is the largest wire gauge size that may be connected to the control power input terminal blocks.
	** The minimum input voltage allowed is 18 V at the power input of the control. The length, size of wire, and load current will determine the minimum supply output voltage. The minimum supply voltage measured at the source should always be greater than 18 V. Example: two (source and return) 20 foot (6 m) lengths of 14 AWG (2 mm ²) wire carrying 1.2 A (maximum rated current) will result in a voltage drop from source output to control power input of approx. 0.16 volts. The resulting supply voltage from the example must be greater than 18.16 volts.

Input Power Wiring Diagram

The power supply and ground connections are located on the power supply board.



Figure 2-4. Input Power Wiring Diagram

Chapter 3. Power Supply Board

General Description

The AtlasSC[™] power supply contains the power supply and twelve discrete output driver channels. The discrete outputs are low-side drivers having short circuit and thermal shutdown protection. The discrete output drivers are not isolated from each other, and are powered by an external +12 Vdc or +24 Vdc. They are isolated from the internal power supplies of the AtlasSC Control platform.

Input power connections are made to the power supply through terminals on the upper edge of the power supply board.



The AtlasSC power supply board must have the input power removed before installing or removing.

This equipment is suitable for use in Class 1, Division 2, Groups A, B, C, and D, or non-hazardous locations only.

Wiring must be in accordance with Class I, Division 2 wiring methods and in accordance with the authority having jurisdiction.



Figure 3-1. Power Supply Board (601-1008)



Specifications

Power Supply Input (Power Supply Board)

Range	18–32 Vdc
Input Current	1.5 A @ 24 Vdc
	1.9 A @ 18 Vdc
Input Power	less than 35 W at 70 °C
Interrupt Time Holdup	8 ms @ >/= 24 V
Efficiency	70% minimum over operating input voltage range
Reverse Polarity Protection	56 V
Input Wiring Constraints	The AtlasSC control platform shall be wired such that no other device receives power from the wiring between the AtlasSC Control Platform and the power supply source.

Discrete Output Drivers (Power Supply Board)

Number of channels	12
Туре	Low-side driver with short circuit and overvoltage protection
Current drive rating	200 mA
Discrete Output Supply Voltage	9–32 V

Power Supply Monitoring Circuit (Power Supply Board)

L۱	/dc Maximum voltage r	neasured	35 Vdc
	Resolutio	on in volts	0.15 Vdc
Maximum E	Error due to temperatur	e change	1.0 Vdc
Ma	ximum Error due to loa	d change	1.0 Vdc
	Total maximum erro	r at 25 °C	1.2 Vdc
	(over 15 to 35	V range)	

Electric Shock

The AtlasSC control platform will not present an electrical shock hazard to the operator or maintenance personnel when used in a normal manner per the National Electrical Code Handbook, ANSI/NFPA 70 HANDBOOK-1990. Safety is ensured by certification through the safety agencies specified in the "Regulatory Compliance" section of this document.

Troubleshooting Guide

Power Supply Checks

The following is a troubleshooting guide for checking areas, which may present difficulties. If these checks are made prior to contacting Woodward for technical assistance, system problems can be more quickly and accurately assessed.

- Is the input voltage within the control's specified input voltage range (measured at control power supply input)?
- Is the input power free of switching noise or transient spikes?
- Is the power circuit dedicated to the AtlasSC control only?

Discrete Output Checks

The AtlasSC power supply contains twelve discrete output driver channels. The discrete outputs are lowside drivers having short circuit and thermal shutdown protection. The discrete output drivers are not isolated from each other, and are powered by an external +12 Vdc or +24 Vdc. They are isolated from the internal power supplies of the AtlasSC Control platform.

- Is the input power within the range of 9–32 V?
- Is the input free of switching noise or transient spikes?
- Is the power circuit dedicated to the control only?
- Are the individual discrete output lines current limited by external series connected components (example: relay coils) to <200 mA?





Figure 3-2. Discrete Output Wiring Example (Discrete Output Interface Wiring to the Power Supply Board)

Configuration Notes

- Refer to Figure 3-2 for discrete output wiring.
- The discrete output commons are tied together, so each power supply board accepts only one voltage source.
- Power for the discrete outputs must be externally supplied, the external supply must be capable of supplying a voltage between 9–32 V while supplying up to 2.5 A. As specified in the Input Power Wiring Section, branch circuit protection of no more than 250% of the maximum rated current of the load (Discrete Output power input current plus 12 times the maximum Discrete Output channel current) shall be provided. Fuse current rating should not exceed 6.25 A (time delay fuses are recommended).
- Chapter 7 describes a relay module that can be used with these discrete outputs.

Chapter 4. SmartCore Board with Actuators

General Description

This chapter covers SmartCore board part number 5503-293.

Each SmartCore board contains circuitry for two speed sensor inputs, six analog inputs, six analog outputs, 2 proportional actuator drivers, 3 serial ports, and 24 discrete inputs. Each speed sensor input may be from a magnetic pick-up or from a proximity probe, each Analog input may be 4–20 mA or 0–5 V, and two of the serial ports, may be RS-232, RS-422, or RS-485. The other serial port is a dedicated RS-232 port.

Features

- 5 ms update rate
- On-board processor for automatic calibration of the I/O channels
- Analog inputs have 14 bit resolution
- Analog outputs have 12 bit resolution

Physical

The AtlasSC boards connect to each other through either the PC/104 bus connectors, or the power bus connectors. All of the boards are held together and to the chassis, by screws. The Smart Core board is the size of two analog boards.

Specifications

Table 4-1. Digital Speed Sensor Inputs	

Number of channels	2	
	selectable as MPU or proximity probe, by terminal block wiring	
Input frequency	100—24 950 Hz	
Resolution	dependent on frequency, 13 bit minimum at maximum speed	
Accuracy	less than $\pm 0.08\%$ full scale from –40 to +85 °C internal temperature	
Table 4-2. MPU Inputs		
Minimum input amplitude	See Figure 4-2	
Input amplitude	70 V peak-to-peak	
Input impedance	>1.5 kΩ	
Isolation voltage	500 Vac minimum, each channel is isolated from all other channels and from the AtlasSC platform	

Released



Figure 4-1. SmartCore Board with Actuators Connections

	Table 4-3. Proximity Probe Inputs
Voltage threshold	at 16 Vin, duty cycle = 10–80%
	at 24 Vin, duty cycle = $10-80\%$ at 28 Vin, duty cycle = $10-80\%$
Voltage threshold	at 16 Vin, duty cycle = 15–65%
/duty cycle at 3 kHz	at 24 Vin, duty cycle = 10–60%
	at 28 Vin, duty cycle = 10–55%
Input voltage range	16–28 Vdc
Available power	none
Isolation	500 Vac minimum, each channel is isolated from all other
	channels and from the AtlasSC platform
Input frequency	0.5–3 kHz

- A derivative output is provided to the application software. The inherent noise on this output, due to resolution, increases with frequency. At 1000 Hz, it can be as much as 2.5 Hz/s. At 5000 Hz, it can be as much as 12.5 Hz/s. At 10 kHz, it can be as much as 25 Hz/s. At 20 kHz, it can be as much as 80 Hz/s. Typically, at 3600 Hz, noise on the MPU signal due to wiring runs will make the noise contribution due to resolution insignificant.
- No proximity probe power provided.
- If the proximity probe inputs are used, the corresponding MPU inputs must be jumpered.



Figure 4-2. Minimum MPU Voltage Graph

Table 4-4. Analog Inputs

Number of channels	6
Input type	4–20 mA or 0–5 V
Max. input current	25 mA ±5% if configured for 4–20 mA
Max. input voltage	5.0 V \pm 5% if configured for 0–5 V
Common mode	80 dB minimum
rejection	
Input common mode	±11 V minimum
range	
Safe input common	±40 V minimum
mode volt	
Input impedance	200 Ω (±1%)
Anti-aliasing filter	2 poles at 10 ms
Resolution	14 bits
Accuracy @ 25 °C	less than ±0.1% of full scale, 0.025 mA
Temp Drift	171 ppm/°C, maximum (1.1% of full scale, 0.275 mA)
	30 ppm/°C, typical (0.20% of full scale, 0.05 mA)
I/O Latency	2 channels at 1 ms, 2 channels at 3 ms, and 2 channels at 5
	ms

- Loop power for the analog inputs is NOT available.
- 4–20 mA or 0–5 V is selected in the GAP block software, for each input, and selected by terminal block wiring. Both the GAP field, and the wiring must be changed.

IMPORTANT

External loop powered transducers must be individually protected with a 100 mA fuse on each channel.

Table 4-5. Analog Outputs

Number of channels	6
Output type	4–20 mA outputs, non-isolated
Common Mode	15 Vdc ±10%
Voltage	
Max current output	25 mA ±5%
Min. load resistance	0 Ω
Max load resistance	300 Ω at 22 mA
Resolution	12 bits
Accuracy @ 25 °C	less than ±0.1% of full scale, 0.025 mA
Temperature Drift	140 PPM/°C, 0.23 mA maximum
	70 ppm/°C, typical (0.45% of full scale, 0.11375 mA)

Number of channels	2	
Actuator Type	Proportional, non-isolated	
Output Type	4-20 or 20-160 mA, software s	selectable
Isolation	none	
Max current output	25mA ±5%	(4-20 mA output scale)
	200 mA ±5%	(20-160 mA output scale)
Min. load resistance	0 Ω	· · · ·
Max load resistance	300 Ω at 22 mA	(4-20 mA output scale)
	45 Ω at 200 mA	(20-160 mA output scale)
Resolution	12 bits	
Accuracy @ 25 °C	less than ±0.1% of full scale,	
	0.026 mA	(4-20 mA output scale)
	0.2 mA	(20-160 mA output scale)
Readback Accuracy	0.5%	
@ 25 °C		
Temperature Drift	140 PPM/°C maximum,	
	0.24 mA maximum	(4-20 mA output scale)
	1.82 mA maximum	(20-160 mA output scale)
	70 ppm/°C typical	0.45% of full scale,
	0.12 mA	(4-20 mA output scale)
	0.91 mA	(20-160 mA output scale)
Readbacks	Actuator source and return cu	rrents
Dither Current	25 Hz, fixed duty cycle, softwa	are variable amplitude
	Table 4-7. Discrete Inputs	3

Table 4-6. Actuator Outputs

Number of channels	24
Input type	Optically isolated discrete input
Input thresholds	< 8 Vdc = "OFF"
	> 16 Vdc = "ON"
Input current	3 mA @ 24 Vdc
Contact voltage	24 Vdc isolated
Max input voltage	28 Vdc
Isolation voltage	500 Vac, all channels are isolated from the AtlasSC platform

- For EMC compliance, the on-board, isolated, +24 Vdc supply is recommended for use as power to contacts, 100 mA maximum.
- All channels are common to each other. Isolation is with respect to the AtlasSC platform and other I/O types.

Number of channels	3
Channel	1 - RS-232, 9 pin sub D connector
configuration	2 - RS-232/RS-485/RS-422 software configurable, terminal
	block connections
Termination Resistor	Located on the board and are accessible via field wiring.
	Termination resistors are provided for RS-485 and RS-422
	D escribes
	Receive.

Table. 4-8. Serial I/O

SmartCore Board Operation

This board includes no potentiometers and requires no field calibration. A SmartCore board may be replaced with another board of the same part number without any adjustment.

Speed Sensor Inputs

The MPU and proximity probe inputs are read and the speed is provided to the application program. A derivative output is also provided. The speed sensor inputs are filtered by the SmartCore board, and the filter time constant is selectable at 8 milliseconds or 16 milliseconds. Eight milliseconds should be acceptable for most turbine applications, while 16 milliseconds may be necessary for very slow speed applications. The speed range is selected in the application software and determines the maximum speed that the board will detect. The control output of the software will detect a minimum speed of one fiftieth of the speed range. This allows detection of failed speed sensors to help prevent overspeed due to slow update times at very low speeds. The monitor output of the GAP block will read down to 0.5 Hz, regardless of the speed range. An application may use any combination of accepted MPU and proximity probes, and any combination of speed ranges.

The SmartCore board uses speed sensing probes mounted on a gear connected or coupled to the turbine's rotor to sense turbine rotor speed. Any of the board's speed channels accept passive magnetic pickup units (MPUs) or proximity probes. It is not recommended that gears mounted on an auxiliary shaft coupled to the rotor be used to sense speed. Auxiliary shafts tend to turn more slowly than the rotor (reducing speed sensing resolution) and have coupling gear backlash, resulting in less than optimum speed control. For safety purposes, it is also not recommended that the speed sensing device sense speed from a gear coupled to a generator or mechanical drive side of a system's rotor coupling.



Figure 4-3. MPU Interface to the SmartCore Board (wiring example)

A proximity probe may be used to sense very low speeds. With a proximity probe, speed can be sensed down to 0.5 Hz. When interfacing to open collector type proximity probes, a pull-up resistor is required between the supplied proximity probe voltage and the proximity probe input to the SmartCore board.





Configuration Notes

- Refer to Figures 4-3 and 4-4 for speed sensor wiring.
- Each speed input channel can only accept one MPU or one proximity probe.
- Proximity probe power is not provided.
- Proximity probes only—External pull-up resistors are required when interfacing to open collector type proximity probes.
- If the proximity probe inputs are used, the corresponding MPU inputs must be jumpered as shown.

Speed Sensor Input Software Configuration Limitations

- (TxMxR)/60 must be < 25 000 Hz
 - T = gear teeth
 - M =(overspeed test limit setting x 1.2)
 - R = gear ratio

Analog Inputs

The analog inputs may be current or voltage type. If a current input is used, a jumper is installed on the terminal block, and the software must be selected for current. This allows the SmartCore card to use the applicable hardware calibration values. If a voltage input is needed, the jumper must be removed, and the software must be selected for voltage.

All Analog inputs may be used with two-wire ungrounded (*loop powered) transducers or isolated (selfpowered) transducers. All Analog inputs have 10 Vdc of common mode rejection. If interfacing to a nonisolated device, which may have the potential of reaching over 10 Vdc with respect to the control's common, the use of a loop isolator is recommended to break any return current paths, which could produce erroneous readings.



For a 4–20 mA input signal, the SmartCore board uses a 200 Ω resistor across the input.







Figure 4-5b. 4–20 mA Input Interface using External Loop Power (wiring example)





Figure 4-6. 0–5 V Input Interface to the SmartCore Board (wiring example)

Configuration Notes

- Refer to Figures 4-5 and 4-6 for analog input wiring.
- All 4-20 mA inputs have an input impedance of 200 Ω.
- When a 4-20 mA input is used, a terminal block jumper must be installed, per Figure 4-5.
- The application software must be configured for a 4–20 mA type input, or a 0–5 V type input.
- Loop power is NOT provided by the Atlas control, it must be sourced externally.

IMPORTANT External loop powered transducers must be individually protected with a 100 mA fuse on each channel.

Analog Outputs

The analog outputs are 4–20 mA with a full scale range of 0–24 mA. The SmartCore board has four analog outputs.






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Configuration Notes

- Refer to Figure 4-7 for analog output wiring.
- Only 4–20 mA signals are output.
- See the specifications section for the maximum analog output load.
- Care should be taken to prevent ground loops and other faults when interfacing to non-isolated devices.
- The output does not contain fault detection. If it is necessary to detect failures, then the device that is driven by the Analog output, for example an actuator driver, must contain reference failure detection.
- The analog outputs have a 15 V common mode voltage, with respect to AtlasSC control common.

NOTICE

Avoid misconnection of the Analog Output (+) to the Actuator Output (-). This will damage internal components, making the control inoperable.

Actuator Outputs

The proportional actuator driver outputs are 4–20 mA or 20-160 mA with a full scale range of 0–24 mA or 0-200 mA. The SmartCore board has two proportional actuator driver outputs, each output with source and return current readbacks.



Figure 4-8. Actuator Output Interface to the SmartCore Board (wiring example)

Configuration Notes

- Refer to Figure 4-8 for actuator output wiring.
- 4–20 mA or 20-160 mA signals are output.
- See the specifications section for the maximum actuator output load.
- Care should be taken to prevent ground loops and other faults when interfacing to non-isolated devices.
- Application software selects the actuator type, the output range, and the dither amount.
- The readbacks can be used in the application software for fault detection.

NOTICE

Avoid misconnection of the Analog Output (+) to the Actuator Output (-). This will damage internal components, making the control inoperable.

Discrete Inputs

The SmartCore board accepts 24 discrete inputs. Contact wetting voltage may be supplied by the SmartCore card. Optionally, an external 18–28 Vdc power source can be used to source the circuit wetting voltage.



Figure 4-9. Discrete Input Interface to the SmartCore Board (wiring example)

Configuration Notes

- Refer to Figure 4-9 for discrete input wiring.
- The discrete input commons are tied together, so each SmartCore board accepts only one voltage source, which can be internally or externally supplied.
- All contact inputs accept dry contacts.
- If an external power supply is used, it must be rated to 28 Vdc max from Class 2 type source for North America (SELV type source for applications outside North America). Power supply outputs must be fused with appropriately sized fuses (a maximum, current rating of 100 ÷ V, where V is the supply's rated voltage, or 5 A, whichever is less).
- The 24 V isolated contact power is protected by a 0.2 A poly switch. This may not prevent interruption in control operation due to a short in the field wiring, but should protect the control from damage. The poly switch will reset itself when the short condition is resolved.

Serial (Modbus) Port Protocols

There are three serial communication ports on the AtlasSC platform that may be used for the Modbus interface. Modbus can use RS-232, RS-422, and RS-485 configurations. Shielded cables and Serial Port Isolator/Converter(s) are required when using these ports. The first two ports on the SmartCore board may be configured for RS-232, RS-422, or RS-485, and the last port is configurable for RS-232 only.

RS-232—An ANSI (American National Standards Institute) standard definition of electrical, functional and mechanical connections for communications between DTE (Data Terminal Equipment) and DCE (Data Communications Equipment) such as connection of a computer to a modem. It has gained wide usage in short-distance applications (15 m/50 ft). In practice, the standard is largely ignored beyond the most rudimentary implementation of electrical signals (\pm 3 to \pm 15 volts). Woodward's implementation will support speeds up to 115K baud in Atlas systems. The actual specification allows 19.2K baud at up to 15 m (50 ft).

RS-422—Also an ANSI standard definition of electrical connections for communications between devices. Because it uses balanced drivers, it can communicate over long distances (1200 m/4000 ft) at high baud rates (115K). Woodward's implementation of RS-422 is actually a 4-wire RS-485 communications network. Since Woodward convention has been to call this

RS-422, this manual will follow that convention. However, it may be important to understand that the actual port is RS-485. The port supports up to 32 devices as a full duplex, multi-drop communications network. This allows more than one device to be connected to the common bus with a single master requesting data. It requires two twisted pairs and ground to operate.

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RS-485—Also an ANSI standard definition of electrical connections for communications between devices. Because it uses balanced drivers it can communicate over long distances (1200 m/4000 ft) at high baud rates (115K). This protocol is implemented identically to "RS-422" with the exception that only one twisted pair is required. Both transmitted and received data use the same pair of wires. A ground wire is still required since the output is isolated. The port supports up to 32 devices as a half duplex, multi-drop communications network. This allows more than one device to be connected to the common bus with a single master requesting data. It requires one twisted pair and ground to operate.

Interface Cables and Connectors

When choosing a cable for RS-485, it is necessary to examine the required distance of the cable and the data rate of the system. Beyond the obvious traits such as number of conductors and wire gauge, cable specifications include a handful of less intuitive terms.

Characteristic Impedance (ohms)—A value based on the inherent conductance, resistance, capacitance, and inductance of a cable that represents the impedance of an infinitely long cable. When the cable is cut to any length and terminated with this Characteristic Impedance, measurements of the cable will be identical to values obtained from the infinite length cable. Therefore, termination of the cable with this impedance gives the cable the appearance of being infinite length, allowing no reflections of the transmitted signal. When termination is required in a system, the termination impedance value should match the Characteristic Impedance of the cable.

Shunt Capacitance (pF/ft)—The amount of equivalent capacitive load of the cable, typically listed in a per foot basis (1 pF/ft = 3.28 pF/m). One of the factors limiting total cable length is the capacitive load. Systems with long lengths benefit from using low capacitance cable.

Propagation velocity (% of c)—The speed at which an electrical signal travels in the cable. The value given typically must be multiplied by the speed of light (c, $3x10^8$ m/s) to obtain units of meters per second. For example, a cable that lists a propagation velocity of 67% gives a velocity of 0.67 x $3x10^8 = 2.01x10^8$ m/s. The higher the percentage, the smaller the signal delay.

General recommendations for serial cable are listed in the following table.

Table 4-9. Serial Cable Requirements

Impedance:	100 Ω ±20%
Cable capacitance:	52.5 pF/m (15.0 pF/ft) at 1 kHz
Propagation Velocity:	67.0%
Data Pairs;	0.2 mm ² (24 AWG) solid bare
	copper
Signal attenuation:	6.0 dB maximum

Recommended Bulk Cable

For simplicity, Woodward will recommend cables consistent with long cable lengths and high baud rates. Longer cable lengths may be possible at lower baud rates with the best cables but are not supported by the ANSI standards for

RS-485. The same cables may be used for RS-232 applications, but length will be limited to 15 m (50 ft).

Ethernet Category 5 cable is a very good cable selection for RS-485 networks. It will support cables lengths to 1200 m (4000 ft) at baud rates up to 115,200. Since Ethernet cable is easy to find and is inexpensive, it will often be the cable of choice. Always use shielded cable.

Installations with high electrical interference (noise) and/or long cable lengths together with high baud rates may benefit from larger gauge cable.

Network Construction

While there are a number of different ways to physically connect devices on a network, Woodward recommends that multi-drop networks be constructed using a "daisy chain" configuration or a "backbone with stubs" for best performance. In a daisy chain, wires are run from device one to device two to device three, etc. In a backbone with stubs, a main trunk line is run between the two devices that are physically farthest apart, and then stub lines are run from the intermediate devices to the trunk line. Stubs should be kept as short as possible. See Figure

4-13 for a graphical representation.

Termination

To achieve best performance with RS-485 serial communication networks, it is necessary to terminate the network to prevent interference caused by signal reflections. RS-232 networks are short enough that termination is not required. For ease of setup, Woodward has provided built-in network termination resistors on the SmartCore board for the RS-485 serial ports. To activate the termination resistors, an external jumper must be placed at the connector. See Figures 4-15 and 4-18 for jumper location. The resistor network used is a special design intended to provide maximum noise immunity. The same design should be used at the opposite end of the network. See the circuit diagram (Figure 4-12). This termination network is necessary due to limitations of the Modbus protocol.

Termination resistors must be installed only on the two units that are at the physical ends of the network. Terminating other midpoint units can overload the network and put it into a cyclic thermal shutdown mode. As a rule, no matter how many units are on a network, there should never be more than two terminations installed. For 2-wire networks, a termination network should be used at the two physical ends of the network. For 4-wire networks, Woodward has elected to use a termination network only across the receiver lines at either end of the network. This is feasible due to the baud rate limit of 115K. In either case (2- or 4-wire configurations), a total of two termination networks are used.

Shielding

Shielded cable is required between the AtlasSC control and any other devices. Unshielded cables and improperly shielded cables are likely to cause communication problems and unreliable control operation. The shield must always be ac-coupled (connected through a capacitor) at one end and connected directly to earth on the opposite end for proper operation. The SmartCore board has been constructed so that the serial port Shield connections are directly connected to the Atlas chassis ground (which must be tied directly to earth). Devices connected to the opposite end of the cable must provide for the ac-coupled shield connection.

Woodward has found that a 0.01 μ F capacitance is typically adequate for this purpose. A minimum rating of 1000 Vdc on the capacitor is required.

Serial I/O Ports

There are three serial ports on the AtlasSC control. They may all be used simultaneously and may all be configured independently. Performance of any one port will depend on how many ports are in use and the port traffic. Ports 1 and 2 may be configured as RS-232, RS-422, or RS-485 to match the intended application. All ports are optically isolated from each other.

Any port configured as RS-232 will meet the RS-232C standard for communications. The wiring shall meet the requirements of EIA RS-232. This standard states a maximum cable length of 15 meters (50 feet) with a total capacitance less than 2500 pF and a data rate not exceeding 56 kbps. All AtlasSC serial ports may be configured for data rates up to 115 200 bps but may not function properly at this high data rate when a full 15 meters of cable length is used. Do not leave an RS-232 null modem cable attached to the AtlasSC control if it is removed from the PC, to avoid the possibility of EMC noise being introduced to the control.

Any port configured as RS-422 or RS-485 meets the ANSI standards for this type of communications. The wiring shall be one continuous cable between nodes and shall meet the requirements of EIA RS-422

or EIA RS-485 for 500 kbps. However, one half of the cable length limits is recommended due to harsh environments typical of prime mover installations as follows:

Table 4-10	Cable	l enath	L imits
	. Oubic	Longui	

Cable	Length	Example
Standard shielded twisted pair cable	30 m (100 ft)	
24 AWG, low-capacitance cable	75 m (250 ft)	Belden 9841
22 AWG, low-capacitance cable	120 m (400 ft)	Belden 3105A
20 AWG, low-capacitance cable	150 m (500 ft)	Belden 89207
Fiber optic cable with optical repeaters	> 150 m (500 ft)	







Figure 4-11. RS-422 Interface to the SmartCore Board (wiring example)



(wiring example)



Figure 4-13. Alternate Multipoint Wiring (wiring example without a separate signal ground wire for the SmartCore board)

Configuration Notes

- Refer to Figure 4-10 for RS-232 wiring. The transmit data (TXD), receive data (RXD), and signal ground (SIG GND) must be properly connected as shown. In addition the shield (SHLD) should be connected in at least one location.
- Refer to Figure 4-11 for RS-422 wiring. The transmit data pairs (422T+ and 422T-), receive data pairs (422R+ and 422R-), and signal ground (SIG GND) must be properly connected as shown. In addition, the shield (SHLD) should be connected in at least one location. The last unit in the network chain, and only the last unit, should have it's receiver terminated with a resistor. The AtlasSC has resistors built in (TERM RES) for this purpose. The unit at each end of the network should be terminated.
- Refer to Figure 4-12 for RS-485 wiring. The data lines (485+ and 485–) and signal ground (SIG GND) must be properly connected as shown. In addition, the shield (SHLD) should be connected in at least one location. The last unit in the chain, and only the last unit, should be terminated with a resistor. The AtlasSC has resistors built in (TERM RES) for this purpose. The unit at each end of the network should be terminated.
- The serial ports must be properly configured in the application software for the appropriate communication parameters.
- Termination resistors are available on the SmartCore board and should be wired across the network at the ends of the line.
- The serial ports are individually isolated from each other, and from the rest of the AtlasSC control. The RS-422 and RS-485 specifications state that a ground wire is needed if there is no other ground path between units. The preferred method for isolated ports is to include a separate wire in the ground cable that connects the circuit grounds together.
- Non-isolated nodes may not have a signal ground available. If signal ground is not available, use the alternate wiring scheme of connecting all circuit grounds of isolated nodes to the shield, and connecting the shield to earth ground at a non-isolated node.

Configurable Port (Com 1)

This port may be configured to use the MODBUS RTU protocol or Woodward's ServLink protocol.

The port configuration is defined in the GAP program and sets configuration for kbps, parity, data, and stop bits.

Wiring Diagram—RS-422 (Com 1)





Figure 4-14. RS-422 Wiring Diagram (Com 1)

Wiring Diagram—RS-485 (Com 1)

Serial #1 - RS-485



Figure 4-15. RS-485 Wiring Diagram (Com 1)

Wiring Diagram—RS-232 (Com 1)



Figure 4-16. RS-232 Wiring Diagram (Com 1)

Modbus Port (Com 2)

This port may only be used as a MODBUS RTU port.

The port may be configured for RS-232, RS-422, or RS-485 by using the correct wiring connections and selecting the correct configuration mode. The port configuration is defined in the GAP program and sets configuration for kbps, parity, data, and stop bits.

Serial #2 - RS-422

Wiring Diagram—RS-422 (Com 2)

Atlas 92 RS 422 91 Transceiver 422R 422T 90 Twisted Pairs 422R 89 ~~~~~~~~~ 422T 243 88 RS 422 Device 140 Isolated +5V ● 87 ~~~-• Jumpers to 243 use optional termination 86 resistor 85 84 Isolated Ground 83

Figure 4-17. RS-422 Wiring Diagram (Com 2)

Wiring Diagram—RS-485 (Com 2)





Figure 4-18. RS-485 Wiring Diagram (Com 2)

Wiring Diagram—RS-232 (Com 2)



Figure 4-19. RS-232 Wiring Diagram (Com 2)

RS-232 Configuration Port (Com 3)

Since this port is the only one that can be used to download application code, we recommend that this port be used only as a Woodward ServLink port. Its primary purpose is for configuration using a PC with Woodward's Watch Window. However, it may also be connected to a PC based HMI for local configuration and monitoring.

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The port is fixed as an RS-232 port only. The RS-232 port configuration is set in the GAP program. A Null Modem cable must be used to connect to this port. The AtlasSC has a standard DB-9 Female receptacle. It is best to use a metal shell connector at both ends with the shell connected to the cable shield but isolated from the ground signal wire. The shell of the 9-pin D-sub on the AtlasSC is chassis grounded.

Wiring Diagram—RS-232 (Com 3)



Figure 4-20. RS-232 Wiring Diagram (Com 3)

Fault Detection (Board Hardware)

Number of LED

Each SmartCore board has a red fault LED that is turned on when the system is reset. During initialization of a board, which occurs after every CPU reset, the CPU turns the Fault LED on. The CPU then tests the board using diagnostic routines built into the software. If the diagnostic test is not passed, the LED remains on or blinks. If the test is successful, the LED goes off. If the fault LED on a board is illuminated after the diagnostics and initialization have been completed, the SmartCore board may be faulty or may have the address DIP switches configured incorrectly. The DIP switch setting must match the module address set in the GAP application program. The DIP switch is factory set and should not be changed.

Table 4-11. SmartCore F	ailure Codes
-------------------------	--------------

Flashes	Failure
1	CPU Failure (Clock Fail, Invalid Reset, Halt, etc.)
2	Unexpected CPU Exception (Bus Error, Address Error, Trap, etc.)
3	RAM Error
4	Watchdog Timeout
5	EEPROM Error
6	FLASH Memory Error
7	Operating System Error
8	Stack Memory Overflow
9	Application Checksum Error
10	Communication Error (68302 Fail)
11	Real Time Clock Error
12	Rate Group Slip Error
13-15	Unused
16	Data Log Error
17	NV Log Error
18	Math Exception

Fault Detection (I/O)

In addition to detecting board hardware faults, the application program may detect I/O faults.

- Analog Input Faults—The application software may set a high and low latch set point to detect input faults.
- Speed Sensor Input Faults—The application software may set a high and low latch set point to detect input faults.
- Serial Port Faults—The system monitors the serial communications on the three serial ports, for various communication errors.
- Microcontroller Faults—The system monitors a software watchdog, a hardware watchdog, and a software watchdog on the PC/104 bus communications. All outputs are shutdown in the event of a microcontroller fault.

Troubleshooting Guide

Speed Sensor Inputs

MPUs—If a magnetic pickup input is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Measure the input voltage on the terminal block. It should be greater than 1 Vrms.
- Verify that the signal waveform is clean and void of double zero crossings.
- Verify that no ground connection exists and that the resulting 60 Hz signal is absent.
- Measure the frequency. Frequency should be in the range of 100 Hz to 25 kHz.
- Check the wiring. Look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Check the software configuration to ensure that the input is configured properly.

After verifying all of the above, the AtlasSC should be returned for service.

Proximity Probes—If a proximity probe input is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Measure the input voltage on the terminal block. It should be in the range of 16–28 V peak, and the duty cycle should be within the specified range for the input voltage.
- Verify that the signal waveform is clean and void of double zero crossings.
- Verify that no ground connection exists and that the resulting 60 Hz signal is absent.
- Measure the frequency. Frequency should be in the range of 0.5 Hz to 3 kHz.
- Check the wiring. Look for a loose connection at the terminal blocks and disconnected or misconnected cables. If an open collector probe is used, check to ensure that the pull-up resistor is installed properly.
- Check the software configuration to ensure that the input is configured properly.
- Verify that the corresponding MPU input is jumpered.

After verifying all of the above, the AtlasSC should be returned for service.

Analog Inputs

If an Analog input is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Measure the input voltage on the terminal block. It should be in the range of 0–5 V.
- Verify that there are no or minimal ac components to the analog input signal. AC components can be caused by improper shielding.
- Check the wiring. If the inputs are reading 0 or the engineering units that correspond to 0 mA, look for a loose connection at the terminal blocks and disconnected or misconnected cables. If the unit is a 4–20 mA input, check for proper jumper installation on the terminal block.
- If all of the inputs are reading high, check that the power is not connected across the input directly.
- Check the software configuration to ensure that the input is configured properly.

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If the input is loop powered, ensure that power is provided externally, the Atlas control does not
provide this power.

After verifying all of the above, the AtlasSC should be returned for service.

Analog Outputs

If an Analog output is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Check the load resistance, ensure that it is less than the specification limit for the output current.
- Check to ensure that the load wiring is isolated.
- Check the wiring, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Disconnect the field wiring and connect a resistor across the output. If the output is correct across the resistor, there is a problem with the field wiring.
- Check the software configuration to ensure that the output is configured properly.

After verifying all of the above, the AtlasSC should be returned for service.

Proportional Actuator Outputs

If an Actuator output is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Check the load resistance, ensure that it is less than the specification limit for the output current.
- Check to ensure that the load wiring is isolated.
- Check the wiring, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Disconnect the field wiring and connect a resistor across the output. If the output is correct across the resistor, there is a problem with the field wiring.
- Check the software configuration to ensure that the output is configured properly.

After verifying all of the above, the AtlasSC should be returned for service.

Discrete Inputs

If a discrete input is not functioning properly, verify the following:

- Measure the input voltage on the terminal block. It should be in the range of 18–28 Vdc.
- Check the wiring, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Check the software configuration to ensure that the input is configured properly.

After verifying all of the above, the AtlasSC should be returned for service.

Serial I/O

If a serial port is not functioning properly, verify the following:

- Check the wiring, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Check to be sure that termination resistors are properly installed where needed on the network.
- Check the software configuration to ensure that the input is configured properly.
- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.

After verifying all of the above, the AtlasSC should be returned for service.

Chapter 5. Analog Combo Board

General Description

Each Analog Combo board contains circuitry for two speed sensor inputs, fifteen analog inputs, one cold junction, and two Analog outputs. The first eleven analog inputs may be 4–20 mA inputs or thermocouple inputs, and the remaining four analog inputs may be 4–20 mA inputs or RTD inputs.

Features

- 5 ms update rate
- On-board processor for automatic calibration of the I/O channels
- Analog inputs have 15 bit resolution
- Analog outputs have 12 bit resolution
- First 11 analog inputs are software configurable 4–20 mA or thermocouple (in pairs)
- Last 4 analog inputs are software configurable 4–20 mA or RTD (individually)
- First 11 analog inputs are isolated as a group, from the other inputs, and from control common
- Last 4 analog inputs are isolated as a group, from the other inputs, and from control common
- A cold junction measurement is provided on the board



Figure 5-1. Analog Combo Board Connections

Physical

The AtlasSC Analog Combo board connects to the CPU board through the PC/104 bus. It does not connect to the AtlasSC power bus directly, it requires a SmartCore board for this purpose.

Specifications

Number of channels	11								
Input type	4–20 mA,	type J o	or type k	<, therm	locouple	(require	ed)		
Max. input current	24 mA if c	configure	ed for 4-	-20 mA					
Max. input voltage	±72.8 mV	for ther	mocoup	les inpu	uts				
	Type E:-9 °C/1746.6	9.83 mV 88 °F)	(–267.6	68 °C/-4	49.82 °	F) to 72	.8 mV (9	952.60	
	Type J:-8 °C/2191.8	Type J:–8.09 mV (–209.72 °C/–345.50 °F) to 69.55 mV (1199.94 °C/2191.89 °F)							
	Type K:-6 °C/2501.2	6.45 mV 6 °F)	(–263.9	95 °C/–4	43.11 °	F) to 54	.88 mV	(1371.8	1
Thermocouple Range	Type N:4 °C/2371.8	4.34 mV 86 °F)	(–263.2	14 °C/–4	141.65 °	F) to 47	.51 mV	(1299.9	2
	Type R:(°C/3214.1).22 mV ∣8 °F)	(-48.27	7 °C/–54	4.89 °F)	to 21.10) mV (17	767.88	
	Type S:-0 °C/3213.9).23 mV)7 °F)	(–48.60) °C/–55	5.48 °F)	to 18.69	9 mV (17	767.76	
	Type T:-6 °C/751.95	6.25 mV 5 °F)	(–265.7	′1 °C/–4	46.28 °I	F) to 20	.87 mV	(399.97	
Common mode rejection	 -80 dB minimum for analog inputs -96 dB typical for analog inputs -110 dB minimum for thermocouple inputs 120 dB typical for thermocouple inputs 								
Input common mode range	±11 V mir	nimum							
Safe input common mode volt	±40 V mir	nimum							
Input impedance	103 Ω (±1	%) for 4	–20 mA	inputs					
Anti-aliasing filter	2 poles at	: 10 ms ((channe	l 11 has	3 2 poles	at 5 m	s)		
Resolution	15 bits				•		,		
	T/C type				Ra	nge			
		< 25	5 °C	> 2	5 °C	< 30	0 °C	> 30	0 °C
		Тур	Max	Тур	Max	Тур	Max	Тур	Max
Thermocouple Input Accuracy	E or K	0.21	0.80	0.14	0.60				
@ 25°C (%)	J	0.16	0.65	0.11	0.55				
_ 、 ,	N	0.23	0.95	0.18	0.65				
	R or S					0.35	0.85	0.30	0.65
	Т	0.70	1.85	0.35	0.95				
Note—The Atlas may experien	ce degrade	d perfor	mance	of the th	ermoco	uple (T(C) inpute	s of the	

Note—The Atlas may experience degraded performance of the thermocouple (TC) inputs of the Analog Combo cards from ~900 MHz to 1.1 GHz at field strengths greater than 10 V/m. Fields of 10–20 V/m degrade the steady-state performance from a 0.25% tolerance to a 1.2% tolerance. Installation of the Atlas in a metal cabinet will minimize this degradation.

4–20 mA Input Accuracy @	Input type	Тур	Max
25°C (%)	4–20 mA	0.20	0.34

Note—The Atlas may experience degraded performance of these 4–20 mA inputs of the Analog Combo cards from 410 MHz to 450 MHz at field strengths greater than 10 V/m. Fields of 10–20 V/m degrade the steady-state performance from a 0.1% tolerance to a 0.36% tolerance. Installation of the Atlas in a metal cabinet will minimize this degradation.

	Input Type	Typ (ppm/°C)	Typ error (%)	Max (ppm/°C)	Max error (%)
Temperature Drift	E or J	40	0.25	235	1.53
	K or N	55	0.35	235	1.53
	R, S, or T	135	0.90	235	1.53
	4–20 mA	105	0.68	160	1.04

IMPORTANT

For thermocouple inputs, percent error is for positive full scale for 65 degree delta (25 to -40 °C). For 4-20mA inputs, percent error is for 25 mA full scale input for 65 degree delta (25 to -40 °C).

CJ update time	5 ms
Latency	1 ms for odd channels and 3 ms for even numbered channels
Failure detection	Open wire detection provided for thermocouples
	All input channels are isolated from the rest of the AtlasSC platform to
Isolation	500 Vac, however they are not isolated from each other. The inputs are
	differential, with a high impedance between channels.
	±3 °C worst case at 25 °C, (±1 °C typical at 25 °C) uncalibrated
CLacouraov	±1 °C worst case at 25 °C, (±0.5 °C typical at 25 °C) calibrated
CJ accuracy	±4 °C over the full temperature range (–40 to +85 °C) uncalibrated
	±2 °C over the full temperature range (–40 to +85 °C) calibrated
I oon power for the analog inp	uts must be supplied by an external supply, if needed

4–20 mA, or type J or K T/C is selected in the GAP block software, for each input. GAP configuration sets input gain via software.

Maximum wire size, one 16 AWG (1.5 mm²), or two 20 AWG (0.5 mm²) wires.

Channels 1–10 must be configured in pairs, that is, channels 1 and 2, 3 and 4, etc., must both be configured as 4–20 mA inputs or must both be configured as thermocouple inputs.

Any "unused" channel of a pair, Channels 1–10, must have its input shorted to prevent measurement errors on the "in-use" channel of the pair.

IMPORTANT

External loop powered transducers must be individually protected with a 100 mA fuse on each channel.

Table 5-2. RTD/4-20 mA Analog Inputs

Number of channels	4
Input type	100 or 200 Ω 3-wire
Max. input current	24 mA, if configured for 4–20 mA
Max. input resistance	781 Ω , if configured for RTD
RTD range	100 Ω RTD: 18.49 Ω (–200 °C/–328 °F) to 390.48 Ω (850 °C/1562 °F)
European Curve (Type 385):	200 Ω RTD: 37.04 Ω (–200 °C/–328 °F) to 533.10 Ω (457 °C/854.6 °F)
RTD range American Curve (Type 392):	100 Ω RTD: 59.57 Ω (–100 °C/–148 °F) to 269.35 Ω (457 °C/854.6 °F) 200 Ω RTD: 119.14 Ω (–100 °C/–148 °F) to 538.70 Ω (457 °C/854.6 °F)
Common mode rejection	 -80 dB minimum for analog inputs -96 dB typical for analog inputs -96 dB minimum for RTD inputs -115 dB typical for RTD inputs
Input common mode range	±11 V minimum
Safe input common mode volt	±40 V minimum

Input impedance	103 Ω (±1%) for 4–20) mA inputs				
Anti-aliasing filter	2 poles at 10 ms	•				
Resolution	15 bits					
	RTD and 4	4–20 mA Input A	ccuracy @ 25 °C	(%)		
		Input type		Тур	Max	
Accuracy	100 Ω American a	nd European, 20	0 Ω American	0.13	0.54	
	20	200 Ω European 0				
		4–20 mA				
	Temperature	e Drift				
Input Type	Typ (ppm/°C)	Typ error (%)	Max (ppm/°C)	Max err	or (%)	
100 Ω European Curve	30	30 0.20 290				
100 Ω American Curve	50	50 0.33 290				
200 Ω European Curve	20 0.13 290				9	
200 Ω American Curve	20	0.13	290	1.8	9	
4–20 mA	105	0.68	160	1.0	4	

For RTD inputs, percent error is for full scale for 65 degree delta (25 to -40 °C). For 4–20 mA inputs, percent error is for 25 mA full scale input for 65 degree delta (25 to -40 °C).

Update time	5 ms
I/O Latency	1 ms
Isolation	All input channels are isolated from the rest of the AtlasSC platform to 500 Vdc, however inputs are not isolated from each other.

- Loop power for the Analog inputs must be supplied by an external supply if needed.
- 4–20 mA, or RTD is selected in the GAP block software, for each input. GAP configuration sets input gain via software.
- Maximum wire size, one 16 AWG (1.5 mm²), or two 20 AWG (0.5 mm²) wires.

Table 5-3. Digital Speed Sensor Inputs

Number of channels	2, MPU only
MPU Inputs:	
Input frequency	100—24 950 Hz
Minimum input amplitude	See Figure 5-2
Maximum input amplitude	70 V peak-to-peak
Input impedance	>550 Ω
Isolation voltage	500 Vac, each channel is isolated from all other channels, and from
Isolation voltage	the AtlasSC platform
Resolution	dependent on frequency, 13 bit minimum at maximum speed
Accuracy	less than ±0.08% of full scale from –40 to +85 °C internal temperature

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Figure 5-2. Minimum MPU Voltage

- A derivative output is provided. The inherent noise on this output, due to resolution, increases with frequency. At 1000 Hz, it can be as much as 2.5 Hz/sec. At 5000 Hz, it can be as much as 12.5 Hz/sec. At 10 kHz, it can be as much as 25 Hz/sec. At 20 kHz, it can be as much as 80 Hz/sec. Typically, at 3600 Hz, noise on the MPU signal due to wiring runs, will make the noise contribution due to resolution, insignificant.
- Maximum wire size, one 16 AWG (1.5 mm²), or two 20 AWG (0.5 mm²) wires.

Number of channels	2
Output type	4–20 mA outputs, non-isolated
Current output	4–20 mA
Max current output	25mA ±5%
Isolation	0 Vdc
Min. load resistance	Ω 0
Max load resistance	300 Ω at 22 mA
Resolution	12 bits
Accuracy	less than ±0.1% of full scale at 25 °C (after software calibration)
Temperature drift	140 ppm/°C, maximum, =0.23 mA
	70 ppm/°C, typical (0.45% of full scale), =0.11375 mA

- 0–1 mA OUTPUTS ARE NOT SUPPORTED, WITHOUT A GREATER THAN 4 BIT LOSS OF RESOLUTION. RESULTING RESOLUTION WOULD BE 7 BITS.
- Common mode voltage is 15 Vdc.
- Maximum wire size, one 16 AWG (1.5 mm²), or two 20 AWG (0.5 mm²) wires.

AtlasSC Analog Combo Board Operation

This board includes no potentiometers and requires no field calibration. An Analog Combo board may be replaced with another board of the same part number without any adjustment.

Speed Sensor Inputs

The MPU inputs are read and the speed is provided to the application program. A derivative output is also provided. The speed sensor inputs are filtered by the Analog Combo board, and the filter time constant is selectable at 8 milliseconds or 16 milliseconds. Eight milliseconds should be acceptable for most turbine applications, sixteen milliseconds may be necessary for very slow speed applications. The speed range is selected in the application software and determines the maximum speed that the board will detect. The control output of the software will detect a minimum speed of one fiftieth of the speed range. This allows detection of failed speed sensors to help prevent overspeed due to slow update times at very low speeds. The monitor output of the GAP block will read down to .5 Hz, irrespective of the speed range. An application may use any combination of accepted MPUs, and any combination of speed ranges.

The Analog Combo board uses speed sensing probes mounted on a gear connected or coupled to the turbine's rotor to sense turbine rotor speed. Any of the board's speed channels accept passive magnetic pickup units (MPUs) or proximity probes. It is not recommended that gears mounted on an auxiliary shaft coupled to the rotor be used to sense speed. Auxiliary shafts tend to turn more slowly than the rotor (reducing speed sensing resolution) and have coupling gear backlash, resulting in less than optimum speed control. For safety purposes, it is also not recommended that the speed sensing device sense speed from a gear coupled to a generator or mechanical drive side of a system's rotor coupling.



Figure 5-3. MPU Interface to the Analog Combo Board (wiring example)

Configuration Notes

• Refer to Figure 5-3 for speed sensor wiring.

Speed Sensor Input Software Configuration Limitations

- (TxMxR)/60 must be < 25000 Hz
- T = gear teeth
- M = (overspeed test limit setting x 1.2)

R = gear ratio

Analog Inputs

The Analog inputs may be current or temperature inputs. The first 11 inputs can be thermocouple inputs, and the other 4 inputs can be RTD inputs. The software must be configured for the correct input type. This allows the Analog Combo card to use the applicable hardware calibration values, and to configure the appropriate hardware gains. The first 10 inputs must be configured in pairs, that is, channels 1 and 2 must both be thermocouples or must both be 4–20 mA inputs. Channels 11–15 may be configured individually.

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All 4–20 mA inputs may be used with two-wire ungrounded (*loop powered) transducers or isolated (selfpowered) transducers. All Analog inputs have 11 Vdc of common mode rejection. If interfacing to a nonisolated device, which may have the potential of reaching over 11 Vdc with respect to the control's common, the use of a loop isolator is recommended to break any return current paths, which could produce erroneous readings.

RTD inputs must be configured to use either the European or American curve. 200 Ω RTDs are limited to the maximum temperature on the American curve, even when the European curve is used. The RTD source current is 2 ma, and the RTD sense input should be tied to the negative side of the RTD, at the RTD.

See the specifications section for supported thermocouple types. The cold junction sensor is provided on the AtlasSC Analog Combo board. If the actual cold junction in the field wiring occurs elsewhere, the temperature of that junction must be brought into the control as a thermocouple, RTD, or 4–20 mA input, and the application software must be configured to use the appropriate cold junction temperature.

The first 11 analog inputs are isolated as a group from control common, earth ground, and the other 4 analog inputs. The last 4 analog inputs are also isolated as a group from control common, earth ground, and the first 11 analog inputs.

For a 4–20 mA input signal, the Analog Combo board uses a 100 Ω resistor across the input.



Figure 5-4a. 4–20 mA or Thermocouple Input Interface to the Analog Combo Board (wiring example, inputs 1–11)



Figure 5-4b. 4–20 mA Input Interface using External Loop Power (wiring example, inputs 1–11)





Figure 5-5. 4–20 mA Input Interface to the Analog Combo Board (wiring example, inputs 12–15)



Figure 5-6. RTD Input Interface to the Analog Combo Board (wiring example, inputs 12–15)

Configuration Notes

- Refer to Figures 5-4, 5-5, and 5-6 for analog input wiring.
- 4–20 mA inputs are supported, 0–5 V inputs are not.
- The application software must be configured to match the input type used, that is, 4–20 mA, 100 Ω RTD, k-type thermocouple, etc.



External loop powered transducers must be individually protected with a 100 mA fuse on each channel.

RTDs

• Only 100 and 200 Ω RTDs are supported.



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- RTD inputs can use the European or American curve. 200 Ω RTDs are limited to the maximum temperature on the American curve, even when the European curve is used.
- The RTD source current is 2 mA.
- The RTD sense input should be tied to the negative side of the RTD, at the RTD.

Thermocouples

- See the specifications section for supported thermocouple types.
- The cold junction sensor is provided on the AtlasSC Analog Combo board. If the actual cold junction in the field wiring occurs elsewhere, the temperature of that junction must be brought into the control as a thermocouple, RTD, or 4–20 mA input, and the application software must be configured to use the appropriate cold junction temperature.
- The thermocouple and cold junction input units (°C or °F) should be consistent in the application software.

4–20 mA Inputs

- All 4–20 mA inputs have an impedance of 100 Ω .
- No loop power is provided.

IMPORTAN

External loop powered transducers must be individually protected with a 100 mA fuse on each channel.

Analog Outputs

The analog outputs are 4–20 mA with a full scale range of 0–24 mA. The SmartCore board has four analog outputs.



Figure 5-7. Analog Output Interface to the Analog Combo Board (wiring example)

Configuration Notes

- Refer to Figure 5-7 for analog output wiring.
- Only 4–20 mA signals are output.
- See the specifications section for the maximum analog output load.
- Care should be taken to prevent ground loops and other faults when interfacing to non-isolated devices.
- The output does not contain fault detection. If it is necessary to detect failures, then the device that is driven by the Analog output, for example an actuator driver, must contain reference failure detection.
- The Analog outputs have a 15 V common mode voltage, with respect to AtlasSC control common.
- +V is 15 V



Avoid misconnection of the Analog Output (+) to the Actuator Output (-). This will damage internal components, making the control inoperable. This applies only when a SmartCore Board with actuators is installed in the control.

Fault Detection (Board Hardware)

Each Analog Combo board has a red fault LED that is turned on when the system is reset. During initialization of a board, which occurs after every CPU reset, the CPU turns the Fault LED on. The CPU then tests the board using diagnostic routines built into the software. If the diagnostic test is not passed, the LED remains on or blinks. If the test is successful, the LED goes off. If the fault LED on a board is illuminated after the diagnostics and initialization have been completed, the Analog Combo board may be faulty or may have the address DIP switches configured incorrectly. The DIP switch setting must match the module address set in the GAP application program.

Table 5-5. Analog Combo Fault Codes

Number of LED

Flashes	Failure
1	Microprocessor failure
2	Bus, address, any unexpected exception error
5	Failure during EE test or erasing
7	Kernel software Watchdog count error
12	Failure during CPU Internal RAM test
13	Dual port RAM error

Fault Detection (I/O)

In addition to detecting board hardware faults, the application program may detect I/O faults.

- Analog Input Faults—The application software may set a high and low latch set point to detect input faults. For thermocouple inputs, open wire detection is provided.
- Speed Sensor Input Faults—The application software may set a high and low latch set point to detect input faults.
- Microcontroller Faults—The system monitors a software watchdog, a hardware watchdog, and a software watchdog on the PC/104 bus communications. All outputs are shutdown in the event of a microcontroller fault.

Troubleshooting Guide

Speed Sensor Inputs

If a magnetic pickup input is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Measure the input voltage on the terminal block. It should be greater than that shown in Figure 5-2.
- Verify that the signal waveform is clean and void of double zero crossings.
- Verify that no ground connection exists and that the resulting 60 Hz signal is absent.
- Measure the frequency. Frequency should be in the range of 100 Hz 25 kHz.
- Check the wiring. Look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Check the software configuration to ensure that the input is configured properly; check the hertz to rpm ratio, and the maximum speed.

After verifying all of the above, the AtlasSC should be returned for service.

Analog Inputs

If an Analog input is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Measure the input voltage on the terminal block. It should be in the range of 0–5 V for 4–20 mA inputs. RTD inputs have a 2 mA current source. Thermocouple inputs should have the appropriate millivolt signal.
- Verify that there are no or minimal AC components to the Analog Input signal. AC components can be caused by improper shielding. Thermocouple inputs are extremely sensitive to signal fluctuations.
- Check the wiring. For a 4–20 mA input if the input is reading 0 or the engineering units that correspond to 0 mA, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- For RTD inputs, check for proper connection of the sense line.
- For thermocouple inputs, check for proper cold junction location.
- If the input is reading high, check that the power is not connected across the input directly.
- Check the software configuration to ensure that the input is configured properly. Ensure that the proper RTD or thermocouple type is selected, if applicable.

After verifying all of the above, the AtlasSC should be returned for service.

Analog Outputs

If an Analog output is not functioning properly, verify the following:

- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Check the load resistance, ensure that it is less than the specification limit for the output current.
- Check to ensure that the load wiring is isolated.
- Check the wiring, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Disconnect the field wiring and connect a resistor across the output. If the output is correct across the resistor, there is a problem with the field wiring.
- Check the software configuration to ensure that the input is configured properly.

After verifying all of the above, the AtlasSC should be returned for service.

Chapter 6. PowerSense Board

General Description

Each PowerSense board contains the circuitry for two sets of three-phase ac voltage (PT) and ac current (CT) inputs, as well as a speed bias output, a voltage bias output, and a LON communications port.

Features

- On-board processor for automatic calibration of the I/O channels
- PT and CT inputs provide fundamental as well as harmonic information
- PT and CT inputs are updated after 3 cycles, which is 50 ms at 60 Hz
- PT and CT inputs and bias outputs have 12 bit resolution
- PT inputs are software configurable for 70 V, 120 V, or 240 V ranges
- Each set of PT and CT inputs is isolated from the rest of the board and chassis
- 5 ms update rate for speed and voltage bias outputs
- Speed bias output is software configurable for 4–20 mA, 0–5 V, PWM, or ±3 V output
- Speed Bias output is isolated from the rest of the board
- Voltage Bias output is software configurable for 4–20 mA, ±1 V, ±3 V, and ±9 V
- Voltage bias output is isolated from the rest of the board
- The LON port is isolated from the rest of the board

Physical

The AtlasSC[™] PowerSense board connects to the CPU board through the PC/104 bus. It connects to the power supply, through the AtlasSC power bus. It requires a SmartCore board or a Pentium CPU board as a master controller. The master controller is responsible for all application level tasks such as protective relaying, load share, and controlling the voltage and speed bias outputs.

Hazardous Live

The following circuits are classified as Hazardous Live because they carry potential shock hazardous voltages during normal operation or under single fault conditions:

- potential transformer (PT) inputs
- current transformer (CT) inputs
- voltage bias outputs



HIGH VOLTAGE—Do not contact the above inputs and outputs during system operation when such circuits are live. Possible serious personal injury or death could result.





HIGH VOLTAGE—To prevent risk of electric shock, make sure that the terminal block covers are installed on the above inputs before operation (see Figure 6-1).

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Figure 6-1. Terminal Block Covers

These inputs and outputs are provided with 500 V of dielectric isolation from chassis ground. In addition, these inputs/outputs are isolated from safety extra-low voltage (SELV) circuits (such as serial communication, PC/104 circuits) by optoisolators or transformers provided with double insulation and 3 kVac of dielectric isolation.

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Figure 6-2. PowerSense Board Connections



Specifications

Table 6-1. Utility and Generator PT Voltage Inputs

	-		
Input Voltage (Typical) Max. Input Voltage Input Current Input Frequency Common Mode Voltage Common Mode Rejection Ratio	77, 120, 240/277 Vac 300 Vac 3 mA maximum 45—70 Hz ±450 Vdc minimum –63 dB minimum No harmonics	c, transducers externall Accuracy	y powered.
Accuracy at 25 °C	Voltage	0.25% or	0.175V, 70V scale
Accuracy at 25 C		0.25% or	0.5° , 120^{\circ} scale
	* with CAD act Date		0.6V, 240V Scale
		auit Fillers set at 0.07	
	(vrange = 70/420/240)/rmss)		Any 10 °C shanne
	70/120/240 Vrms)		Any 40 °C change
	i ypical (1σ) :		< 0.3% of Vrange
		0.28V, 70V scale	
PT Temp Drift		0.48V, 120V scale	0.36V, 120V scale
		0.96V, 240V scale	0.72V, 240V scale
	Max (3σ):	< 1.2 % of Vrange	< 0.8% of Vrange
		0.84V, 70V scale	0.56V, 70V scale
		1.44V, 120V scale	0.96V, 120V scale
		2.88V, 240V scale	1.92V, 240V scale
Input Impedance	400 KO or greater	V/F a a attice a la avec	
Isolation	See HAZARDOUS LI	VE Section above	
Table 6-	2 Utility and Generator	CT Current Inputs	
	2. Othey and Concrator		
Input Current	5 A rms full scale		
Max Transient Input Current	7 07 A rms		
Input Frequency	45—70 Hz		
Common Mode Voltage	+250 Vdc minimum		
Common Mode Rejection Ratio	-63 dB minimum		
Accuracy	see PT inputs section at	ove	
,	(Full Scale = 5 Arms)	–40 to +85 °C	Any 40 °C change
CT Temp Drift	Typical $(1\sigma)^{\circ}$	< 0.4% or 20.0 mA	< 0.3% or 15.0 mA
PP	Μax (3σ):	< 1.2% or 60.0 mA	< 0.8% or 40.0 mA
Input Impedance	0.030 Ω		
		the second second	

Isolation see HAZARDOUS LIVE section above

Number of channels	1		
Current output option	4–20 mA selected by software switch and wiring		
Max current output	25mA ±5%		
Voltage output options	0–5 V (5 V max ±5%), ±3 V (±3V limit ±5%), 500 Hz PWM, selected by software switch and wiring		
Isolation	500 Vac		
Max load resistance	300 Ω at 24 mA for 4–20 n	nA output, infinite for V	outputs
Min load resistance	0 Ω for current output, 450 Ω for ±3 V output, 1k Ω for 0–5 V output, PWM output.		
Resolution	11 bits, except for PWM output		
Accuracy	less than $\pm 0.1\%$ of full scale @ 25 °C or $\pm 0.006V$ for $\pm 3V$ output $\pm 0.005V$ for 0–5 V output ± 0.025 mA for 4–20 mA output		
		–40 to +85 °C	Any 40 °C change
Temp Drift	Typical (1σ):	< 0.5% or 0.125 mA	< 0.3% or 0.075 mA
(4–20 mA, FS = 25 mA)	Max (3σ):	< 1.2% or 0.30 mA	< 0.8% or 0.20 mA
Temp Drift	Typical (1σ):	< 0.3% or 18.0 mV	< 0.2% or 12.0 mV
(±3V range)	Max (3σ):	< 0.7% or 42.0 mV	< 0.5% or 30.0 mV
Temp Drift	Typical (1σ):	< 0.1% or 5.0 mV	< 0.1% or 5.0 mV
(0–5 V range and PWM)	Max (3σ):	< 0.2% or 10.0 mV	< 0.2% or 10.0 mV

Table 6-3. Speed Bias Output

Options: 4–20 mA, 0–5 V, ±3V, PWM- 5V- 500 Hz. Both the GAP block field and the wiring must be changed to switch between current and voltage outputs. Only the GAP block field must be changed to switch between the voltage outputs.

Table 6-4. Voltage Bias Output

Number of channels	1			
Current output option	4–20 mA selected by software switch and wiring			
Max current	25mA ±5%			
Voltage output options	±1, ±3, ±9 V selected by software switch and wiring			
Max Voltage	±9V ±5%			
Isolation	see HAZARDOUS LIVE section above			
Max load resistance	300 Ω at 24 mA for 4–20 mA output, infinite for V output			
Min load resistance	7kΩ for ±9V output, 0 Ω for 4–20 mA output			
Resolution	11 bits for $\pm 9V$, >9 bits for $\pm 3V$, >7 bits for $\pm 1V$			
	less than ±0.1% of full scale @ 25 °C or			
Accuracy	±0.018V for ±1V, ±3V, ±	9V output		
	±0.025 mA for 4–20 mA output			
Temp Drift		–40 to +85 °C	Any 40 °C change	
(4-20 mA FS = 25 mA)	Typical (1σ):	< 0.5% or 0.125 mA	< 0.3% or 0.075 mA	
	Max (3σ):	< 1.2% or 0.30 mA	< 0.8% or 0.20 mA	
Temp Drift	Typical (1σ):	< 0.3% or 54.0 mV	< 0.3% or 54.0 mV	
(±9V range)	Max (3σ):	< 0.6% or 108.0 mV	< 0.5% or 90.0 mV	
Temp Drift	Typical (1σ):	< 0.9% or 54.0 mV	< 0.9% or 54.0 mV	
(±3V range)	Max (3σ):	< 1.8% or 108.0 mV	< 1.5% or 90.0 mV	
Temp Drift	Typical (1σ):	< 2.7% or 54.0 mV	< 2.7 % or 54.0 mV	
(±1V range)	Max (3σ):	< 5.4% or 108.0 mV	< 4.5% or 90.0 mV	

Options: $4-20 \text{ mA}, \pm 1\text{V}, \pm 3\text{V}, \pm 9\text{V}$; software and wiring selectable. Both the GAP block field and the wiring must be changed to switch between current and voltage outputs. Only the GAP block field must be changed to switch between the voltage outputs.

Metering Item	Accuracy with Default Filter Values of 0.67
	0.25% or 0.175 V, 70 Vac scale
Voltage	0.25% or 0.3 V, 120 Vac scale
	0.25% or 0.6 V 240 Vac scale
Current	0.25% or 12.5 mA
	0.5% or 1.75 VA, 70 V scale
Power	0.5% or 3.0 VA, 120 V scale
	0.5% or 6.0 VA, 240 V scale
	±0.003 PF
Power Factor	Across entire range of 0.5 lead to
	0.5 lag
Frequency	0.08% of 60 Hz or ±0.048 Hz
Synchronizing	0.25% or ±0.00157 rad or ±0.9°
Harmonics 2-7	1% or 50 mA for current
	1% or 0.7 V, 70 Vac scale
	1% or 1.2 V, 120 Vac scale
	1% or 2.4 V, 240 Vac scale
	2% or 100 mA for current
Harmonics 9, 11, 13	2% or 1.4 V, 70 Vac scale
	2% or 2.4 V, 120 Vac scale
	2% or 4.8 V, 240 Vac scale
Negative Phase	0.5% or 25 mA for current
Sequence Voltage or	0.5% or 0.35 V, 70 Vac scale
Current	0.5% or 0.6 V, 120 Vac scale
Current	0.5% or 1.2 V, 240 Vac scale
Typical Temperature	<0.4% of Vrange for any 40 °C
Drift for voltage inputs	change
Typical Temperature	<0.4% or 20.0 mA for any 40 °C
Drift for current inputs	change

Table 6-5.	PT/CT	Accuracy	/ Table
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* Accuracy values at 25 °C with no harmonics

AtlasSC PowerSense Board Operation

This board includes no potentiometers and requires no field calibration.

A PowerSense board may be replaced with another board of the same part number without any physical adjustment.

PT Voltage Inputs

The PT inputs are designed to sense three phase voltage. All features of the three phase voltage for the generator are exactly duplicated for the mains inputs. The 'A' phase voltage inputs of each set (generator and mains) are used for frequency measurement and synchronizing. The fundamental magnitude and phase information is calculated, in addition to the magnitude and phase of each harmonic. All harmonics are calculated, up to the 7th harmonic, as well as the 9th, 11th, and 13th harmonics. In addition the PT voltage inputs are used in conjunction with the CT current inputs for power calculation purposes. All voltage calculations are performed using algorithms in accordance with IEEE 1459-2000.

Individual fundamental and harmonic voltage inputs are provided. A negative phase sequence voltage input, and a THD voltage input are also provided.

Three hardware ranges are provided and are selected by the application software. The nominal inputs for these ranges are 70 V, 120 V, and 240 V. Using the 70 V range, the lowest voltage that will be sensed it 26.67 V, using the 120 V range the lowest voltage is 40 V, and using the 240 V range the lowest voltage is 80 V.

PT ratio and gain inputs are provided, to allow field configuration of the PT scaling. The PT ratio will scale all three PT inputs by the same ratio. There is a separate gain input provided for each PT input (3 for the generator and 3 for the mains) that is provided to allow for compensation of potential transformer turns ratio inaccuracies.

The PT inputs, have adjustable software filters. All PT inputs are updated every 3 cycles. With an input of 60 Hz, this equates to 50 ms.

The hardware does not require three phases for voltage calculations, the application can configure the module for single phase, and all functionality will be modified accordingly. The single phase input that must be provided is the A phase. The application can also configure the module for a "Y" or "delta" line configuration, and the calculations will compensate appropriately. If the physical connections are to a wye connected generator or load, the input to PowerSense should also be wye connected (line-to-neutral). If the physical connections are to a Delta connected generator or load (no neutral present), the input to PowerSense should also be Delta connected (line-to-line).). The "Y" or "delta" configuration applies to the entire set of PT and CTs, it is not possible to configure the PTs for "Y", and the CTs for "delta", or vice versa. However, the configuration may differ between PT/CT 1 and PT/CT 2 (or gen and mains in a typical configuration).



Figure 6-3. Wye Connected System (wiring example, PT Interface wired L-N to the PowerSense Board)





Figure 6-4. Wye Connected System (wiring example, wired L-N to PowerSense with potential transformers used to step the voltage down to a level within the capability of the PowerSense inputs)





Figure 6-5. Wye Connected System (wiring example, wired L-L to PowerSense with potential transformers used to step the voltage down to a level within the capability of the PowerSense inputs; the ground on the b-phase input is optional.)





Figure 6-6. Delta Connected System (wiring example, PT Interface wired L-L to the PowerSense Board)




Figure 6-7. Delta Connected System

(wiring example, wired L-L to PowerSense with potential transformers used to step the voltage down to a level within the capability of the PowerSense inputs; the ground on the B-phase input is optional.)

Configuration Notes

- Refer to Figures 6-3 through 6-7 for PT wiring.
- When using the 3 different PT ranges, 70 V, 120 V, and 240 V, the physical input must be the correct voltage AND the application software must configure the PT/CT block for the correct voltage range.
- When using a single phase input, the PT input used must be phase A, and the application software
 must configure the PT/CT block for "single phase" operation.
- The application software must configure the PT/CT block for the correct system frequency 50Hz or 60 Hz, as well as for the correct rotation, clockwise or counterclockwise.
- See the GAP help, for information on application software configuration variables.
- The terminal block screws should be torqued to 8 10 in-lbs (0.9038782 1.129848 N-m).

Generator Current Inputs

The CT inputs are designed to sense three phase current inputs. The 'A' phase current inputs of each set are used for phase calculations. The fundamental magnitude and phase information is calculated, in addition to the magnitude and phase of each harmonic. All harmonics are calculated, up to the 7th harmonic, as well as the 9th, 11th, and 13th harmonics. In addition the CT current inputs are used in conjunction with the PT voltage inputs for load calculation purposes. All current calculations implement algorithms in accordance with IEEE 1459-2000.

Individual fundamental and harmonic CT inputs are provided. A negative phase sequence current input and a THD current input are also provided.

The CT inputs are rated at 5A and function down to 50 mA. For optimum accuracy in the usable range, it is recommended to use 5 A secondary CTs with PowerSense (not 1A secondary CTs). Take care to avoid secondary currents greater than 7 Arms, as this will cause damage.

CT ratio and gain inputs are provided, to allow field configuration of the CT scaling. The CT ratio will scale all three CT inputs by the same ratio. There is a separate gain input provided for each CT input (3 for the generator and 3 for the mains) that is provided to allow for compensation of current transformer turns ratio inaccuracies.

The CT inputs have adjustable software filters. All CT inputs are updated every 3 cycles. With an input of 60 Hz, this equates to 50 ms.

The hardware does not require three phases for current calculations, the application can configure the module for single phase, and all functionality will be modified accordingly. The single phase input that must be provided is the A phase. The application can also configure the module for a "Y" or "delta" line configuration, and the calculations will compensate appropriately. If the physical connections are to a wye connected generator or load, the input to PowerSense should also be wye connected (line-to-neutral). If the physical connections are to a Delta connected generator or load (no neutral present), the input to PowerSense should also be Delta connected (line-to-line).). The "Y" or "delta" configuration applies to the entire set of PT and CTs, it is not possible to configure the PTs for "Y", and the CTs for "delta", or vice versa. However, the configuration (line type and single or three-phase) may differ between PT/CT 1 and PT/CT 2 (gen and mains in most systems).

Configuration Notes

- Refer to Figures 6-8 and 6-9 for CT wiring.
- When using only one CT, rather than 3, the CT must be phase A, and the application software must configure the PT/CT block for "single phase" operation.
- The application software must configure the PT/CT block for the correct system frequency 50 Hz, or 60 Hz, as well as for the correct rotation, clockwise or counterclockwise.
- See the GAP help, for information on application software configuration variables.
- The terminal block screws should be torqued to 8–10 lb-in (0.9–1.1 N·m).

Power Calculations

Individual and total watts, VARs, VA, and power factor inputs. A negative phase sequence voltage input and a THD voltage input are also provided.

All power calculations implement algorithms in accordance with IEEE 1459-2000.

The power inputs have adjustable software filters. All power inputs are updated every 3 cycles. With an input of 60 Hz, this equates to 50 ms.

The hardware does not require three phases for load calculations, the application can configure the module for single phase, and all functionality will be modified accordingly. The single phase input that must be provided is the A phase. The application can also configure the module for a "Y" or "delta" line configuration, and the calculations will compensate appropriately. If the physical connections are to a wye connected generator or load, the input to PowerSense should also be wye connected (line-to-neutral). If the physical connections are to a Delta connected generator or load (no neutral present), the input to PowerSense should also be Delta connected (line-to-line). The "Y" or "delta" configuration applies to the entire set of PT and CTs, it is not possible to configure the PTs for "Y", and the CTs for "delta", or vice versa.







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Speed Bias Output

The Speed Bias output is configurable for 4–20 mA with a full scale range of 0–24 mA, or as a \pm 3 V, 0–5 V, or PWM voltage output. When used as a PWM output, the PWM frequency is 500 Hz. Configuring the output requires wiring and application software configuration. This output is isolated from the rest of the control system, to prevent ground loops when connecting the PowerSense board to other controls.

Configuration Notes

- Refer to Figures 6-10 and 6-11 for Speed Bias Output wiring.
- The Speed Bias output must be configured for the correct type, 4–20 mA or the voltage type in the application software.
- See the specifications section for the maximum output load.
- The output does not contain fault detection. If it is necessary to detect failures, then the device that is driven by the analog output, for example an actuator driver, must contain reference failure detection.
- The output cannot be used as a voltage output and a current output at the same time.



Figure 6-10. Speed Bias Output Interface to the PowerSense Board (wiring example, configured for 4–20 mA)



Figure 6-11. Speed Bias Output Interface to the PowerSense Board (wiring example, configured for ±3 V, 0–5 V, or PWM)

Voltage Bias Output

The Voltage Bias output is configurable for 4 - 20 mA with a full scale range of 0 - 24 mA, or as a $\pm 1, \pm 3V$, or $\pm 9V$ voltage output. Configuring the output requires wiring and application software configuration. This output is isolated from the rest of the control system to prevent ground loops when connecting the PowerSense board to other controls. The voltage outputs are configured in the software, therefore the $\pm 9V$ output has about 3 times better accuracy and resolution than the $\pm 3V$ output, which has about 3 times better accuracy and resolution.

Configuration Notes

- Refer to Figures 6-12 and 6-13 for Voltage Bias Output wiring.
- The Voltage Bias output must be configured for the correct type, 4–20 mA or the voltage type, in the application software.
- See the specifications section for the maximum output load.
- The output does not contain fault detection. If it is necessary to detect failures, then the device that is driven by the analog output, for example an actuator driver, must contain reference failure detection.
- The output cannot be used as a voltage output and a current output, at the same time.



Figure 6-12. Voltage Bias Output Interface to the PowerSense Board (wiring example, configured for 4–20 mA)



Figure 6-13. Voltage Bias Output Interface to the PowerSense Board (wiring example, configured for ±1 V, ±3 V, or ±9 V)

LON Channel

The PowerSense board has one LON channel.



Figure 6-14. LON Interface to the PowerSense Board (wiring example)

Configuration Notes

- Refer to Figure 6-14 for LON channel wiring.
- The LON network must be terminated with load terminating resistors.
- The LON channel is DSLC/MSLC and EGCP-3 compatible. It may only be used with specified Woodward equipment.

Fault Detection (Board Hardware)

Each PowerSense board has a red fault LED that is turned on when the system is reset. During initialization of a board, which occurs after every CPU reset, the CPU turns the Fault LED on. The CPU then tests the board using diagnostic routines built into the software. If the diagnostic test is not passed, the LED remains on or blinks. Table 6-1 below explains what a blinking LED indicates. The LED will blink the appropriate number of times, rest for one second, then blink again in repetition. If the test is successful, the LED goes off. If the fault LED on a board is illuminated after the diagnostics and initialization have been completed, the PowerSense board may be faulty or may have the address DIP switches configured incorrectly. The DIP switch setting must match the module address set in the GAP application program. It cannot be the same address used by any other card on the PC/104 bus. These are factory set at the time the system is built.

Table 6-6. PowerSense Fa	ailure Codes
--------------------------	--------------

Number of LED	
Flashes	Failure
Off	No failure, system OK
Solid	Module in initialization mode
1	Hardware watchdog, CPU clock failure, reset fail
2	Unexpected Exception Error
3	RAM test failure
5	EEPROM failure
7	Kernel Watchdog Timeout
10	System Error
11	Board Identification Error
12	TPU RAM failure
13	Dual Port RAM test failure
14	QSM or ADC Initialization failure
15	Self test status failure
20	Invalid A/D converter selected
21	QSPI timeout
24	ADC auto calibration time-out

Fault Detection (I/O)—In addition to detecting board hardware faults, the application program may detect I/O faults, by comparing values against each other at specific operating points. For example, if phases A and C are reading correctly, and phase B is significantly different, the application could annunciate a problem with phase B.

Microcontroller Faults—The system monitors a software watchdog, a hardware watchdog, and a software watchdog on the PC/104 bus communications. All outputs are shutdown in the event of a microcontroller fault or watchdog detection.

Troubleshooting Guide

PT Inputs

If a PT input is not functioning properly, verify the following:

- Measure the voltage and frequency on the terminal block, to ensure that they are correct.
- Check the wiring. Look for a loose connection at the terminal blocks and for disconnected or misconnected cables.
- Check the PT range configuration in the application software, to ensure that it corresponds to the rated voltage at the input.
- Check the system frequency configuration in the application software, to ensure that 50Hz or 60 Hz is selected, as appropriate.
- Check the "3 phase" configuration in the application software. In single phase mode, phases B and C will be set to 0.
- Check the A phase signal. B and C phase PTs will not read correctly without the A phase PT. If the A phase frequency is unstable, B and C readings will be unstable.
- Verify the settings to the PT/CT block in the application software.
- Verify the settings for wye or delta (L-N or L-L) match the actual wiring
- Verify proper grounding. Incorrect grounding practices may cause inaccurate voltage readings.

After verifying all of the above, the AtlasSC should be returned for service.

CT Inputs

If a CT input is not functioning properly, verify the following:

- Measure the current and frequency on the terminal block, to ensure that they are correct.
- Check the wiring. If the input is reading 0, look for a shorted connection at the terminal blocks and for misconnected cables.
- Check the system frequency configuration in the application software, to ensure that 50Hz or 60 Hz is selected, as appropriate.
- Check the "3 phase" configuration in the application software, in single phase mode phases B and C will be set to 0.
- Check the A phase PT signal, CT phases A, B, and C will not read correctly without the A phase PT. If the A phase PT frequency is unstable the CT readings will be unstable.
- Verify the settings to the PT/CT block, in the application software.
- Verify the settings for wye or delta (L-N or L-L) match the actual wiring
- If the CT common is not grounded, try grounding it to eliminate offsets in the readings. Be sure to
 ground it in the same location as the other system grounds following good, accepted grounding
 practices.

After verifying all of the above, the AtlasSC should be returned for service.

Speed and Voltage Bias Outputs

If a speed or voltage output is not functioning properly, verify the following:

- Check to make sure that the output is wired to the correct terminals, 4–20 mA connections are different from voltage output connections.
- Check that the cable is shielded and the shield is properly grounded per the shields and grounding section in Chapter 2.
- Check the load resistance, ensure that it is less than the specification limit for the output current.
- Check the wiring, look for a loose connection at the terminal blocks and disconnected or misconnected cables.
- Disconnect the field wiring and connect a resistor across the output. If the output is correct across the resistor, there is a problem with the field wiring.
- Check the software configuration to ensure that the output is configured properly.

After verifying all of the above, the AtlasSC should be returned for service.

LON Channel

If the LON channel is not functioning properly, verify the following:

- Check the network wiring, it should be low capacitance cable.
- The cable length should not exceed the maximum allowed cable length.
- Check to make sure that termination networks are installed on the ends of the network.
- Check the software configuration.

After verifying all of the above, the AtlasSC should be returned for service.

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Chapter 7. 12-Channel Relay Module

General Information



Figure 7-1. 12-Channel Relay Module

The Atlas system has 12 relay drivers on the power supply board. For customers that do not wish to wire their own discrete relays, Woodward can provide an integrated 12-channel relay module with cable harness that is certified for use in ordinary, hazardous, and marine locations.

The module is DIN rail mounted. Approximate dimensions are 254 mm long, 76 mm wide, and 64 mm tall (10 inches long, 3 inches wide, and 2.5 inches tall).

- Relay Module, Item Number 5441-699
- Cable, Item Number 5417-747

Relay Information

Each relay has one set of normally open contacts and one set of normally closed contacts. The relay contact ratings are:

3.0 Å at 120 Vac (resistive)
2.0 Å at 120 Vac (inductive)
0.5 Å at 120 Vac (lamp or tungsten as UL prefers)
5.0 Å at 28 Vdc (resistive)
0.1 Å at 125 Vdc (resistive)
0.112 hp—28 Vdc (motor)
0.241 hp—120 Vac (motor)

Shielding

There is a terminal on the module labeled "SHLD". A wire should be connected between this terminal and a good local system ground. Alternatively, a ground wire can be crimped to the bare shield wire at the Atlas end of the cable and then tied to the chassis ground stud of the Atlas. If this shield wire is not used at the Atlas end of the cable, it should be trimmed back to the insulation jacket of the cable.

Board Status Lights

The module is equipped with twelve yellow LEDs to indicate when each relay has been energized, and one green LED to indicate that there is external power to the module. For proper operation, the green LED must be lit any time the Atlas system is being used.

Wiring

The relay module requires an external 18 to 32 Vdc power supply and a wiring harness. One end of this cable has been stripped back several inches, and individual wires are labeled with the terminal numbers of the appropriate terminals used on the Atlas power supply board. Refer to the following plant wiring diagram and the power supply chapter of this manual for more details.

Released

Manual 26179



Figure 7-2. 12-Channel Relay Module Connections

Chapter 8. Distributed I/O

General Description

The AtlasSC[™] control has been designed for limited I/O applications. If additional I/O is required, the use of third party distributed I/O modules connected to a serial port on the SmartCore board can be used to augment the AtlasSC I/O. This chapter explains the use of these distributed I/O modules and gives specific examples of two commonly used brands. Distributed I/O modules are limited in their update rates and therefore are not recommended for core control applications. They are ideal for use with secondary limit monitoring, auxiliary controls, and instrumentation display requirements.

This chapter has been divided into two sections. The Serial Interface section contains general information on hardware for end users and software for application developers. The Distributed I/O Example section contains a detailed example of an Automation Direct setup. We hope that this specific example of I/O module configurations can be used as a foundation for building a majority of the AtlasSC control applications where Distributed I/O is needed.

Serial (Modbus) Interface

The AtlasSC control supports industry standard AEG-Modicon Inc.'s Modbus[®] protocol. The Modbus protocol determines how the master and slaves establish and break off contact, how the sender and receiver are identified, how messages are exchanged in an orderly manner, and how errors are detected. The protocol also controls the query and response cycle which takes place between the master and slave devices.

Modbus is an application layer messaging protocol, at level 7, as defined in the OSI (Open System Interconnection) model. It provides client/server communication between devices connected on different types of buses or networks. It is a request/reply protocol and offers services specified by function codes. Modbus function codes are elements of Modbus request/reply PDUs (Protocol Data Unit).

Modicon developed the Modbus addressing scheme around the data table and I/O structure in Modicon PLCs.

Serial Software and Hardware Requirements

- GAP[™] Programming Tool (all versions)
- Serial Cables and Connectors
- Slave Interface and Modules
- Device Memory Maps and Scaling (Manuals)
- Woodward Interface Tools

Serial (Modbus) Port Protocols

There are up to three serial communication ports on the Atlas platform that may be used for the Modbus interface. Modbus can use RS-232, RS-422, and RS-485 configurations. The first two ports on the SmartCore board may be configured for RS-232, RS-422, or RS-485 and the last port is configurable for RS-232 only.

RS-232—An ANSI (American National Standards Institute) standard definition of electrical, functional, and mechanical connections for communications between DTE (Data Terminal Equipment) and DCE (Data Communications Equipment) such as connection of a computer to a modem. It has gained wide usage in very short-distance applications (15m/50 ft). In practice the standard is largely ignored beyond the most rudimentary implementation of electrical signals (\pm 3 to \pm 15 volts). Woodward's implementation will support speeds up to 115K baud in AtlasSC systems. The actual specification allows 19.2K baud at up to 15 m (50 ft).

RS-422—Also an ANSI standard definition of electrical connections for communications between devices. Because it uses balanced drivers, it can communicate over long distances (1200 m/4000 ft) at high baud rates. Woodward's implementation of RS-422 supports data rates up to 115K baud. In addition to the standard RS-422 features, a multidrop function is implemented as well. This allows more than one device to be connected to a common bus (up to 32 devices) with a single master requesting data. It requires two twisted pairs and ground to operate.

RS-485—Also an ANSI standard definition of electrical connections for communications between devices. This protocol is implemented identically to RS422 at Woodward with the exception that only one twisted pair is required. Both transmitted and received data use the same pair of wires. A ground wire may or may not be required, depending on whether the grounds are connected through the chassis or other connections.

Connectors Pinouts

Connector	Terminal	Signal Mnemonic
PHOENIX	SmartCore	
00000000000	with Actuator	
	Pinout	
1	93	232 TXD
2	94	232 RXD
3	95	SIG GND
4	96	422/485 (+) RECEIVE
5	97	TERM RES. +
6	98	TERM RES
7	99	422/485 (–) RECEIVE
8	100	422 (+) TRANSMIT
9	101	422 (1) TRANSMIT
10	102	SHLD (CHASSIS)

Table 8-1. SmartCore SIO # 1 Connector and Pinout

Table 8-2. SmartCore SIO # 2 Connector and Pinout

Connector	Terminal	Signal Mnemonic
PHOENIX	SmartCore	
0000000000	with Actuator	
	Pinout	
1	83	232 TXD
2	84	232 RXD
3	85	SIG GND
4	86	422/485 (+) RECEIVE
5	87	TERM RES. +
6	88	TERM RES
7	89	422/485 (–) RECEIVE
8	90	422 (+) TRANSMIT
9	91	422 (1) TRANSMIT
10	92	SHLD (CHASSIS)

Table 8-3. SmartCore SIO # 3 Connector and Pinout

Connector	Signal Mnemonic
DB9F	Shielded DB9 female receptacle
1	
2	RXD
3	TXD
4	
5	GND
6	
7	
8	
9	
Shield	

Network Wiring

See Chapter 4 for serial I/O wiring.

Network Length

When choosing a transmission line for RS-232, RS-422, or RS-485, it is necessary to examine the required distance of the cable and the data rate of the system. Losses in a transmission line are a combination of ac losses (skin effect), dc conductor loss, leakage, and ac losses in the dielectric.

	Maximum Distances
RS-232	15 m (50 ft)
RS-422, RS-485	1219 m (4000 ft)

Serial (Modbus) Slave Hardware

The AtlasSC control system may control Modbus slave modules from many different manufacturers. With the exception of Woodward-manufactured devices, Woodward makes no expressed or implied statement of suitability of these devices. It is the user's responsibility to ensure EMC compliance of their system, if necessary, by using distributed modules that are CE compliant. The following Serial Modbus slave hardware has been tested with the AtlasSC control to confirm functionality.

1) All Woodward devices designed for Modbus.

2) Automation Direct Terminator I/O

Part Number	Description
T1K-01AC	Power Supply
T1K-01DC	Power Supply
T1K-MODBUS	Modbus Interface
T1K-08ND3	Discrete Input (Sink)
T1K-16ND3	Discrete Input (Sink)
T1K-16TD1	Discrete Output
T1K-08TRS	Discrete Output (Relay)
T1F-16AD-1	Analog Input (Current)
T1F-16DA-1	Analog Output (Current)
T1F-14THM	Thermocouple Input
T1K-08B-1	Terminal Base
T1K-16B-1	Terminal Base

Serial (Modbus) Software



Many end users will be purchasing pre-programmed AtlasSC units and will not need the information in this section. The information below is aimed at programmers using the GAP programming tool provided by Woodward. The information provided here covers the basics.

Programmers will need the following:

- Device memory maps and scaling (Manuals)
- GAP Programming Tool (all versions) to create the application. (This can be downloaded from our website:
- <u>www.woodward.com/software</u>

The AtlasSC is configured to communicate with a serial network by performing the following steps:

- 1. Determine slave and I/O modules to be used.
- 2. Determine order of I/O modules.
- 3. Review the Serial Modbus slave manuals and understand the hardware addressing.
- 4. Create GAP application which reads and writes to the Serial I/O.
- 5. Transfer GAP application to AtlasSC control.
- 6. Start GAP application.

Serial (Modbus) Software Configuration (Protocol)

The AtlasSC supports both the ASCII and RTU versions of the Modbus protocol. RTU is more widely used since it is the more efficient of the two.

- ASCII: hex coding / 7 bits per character (4 transmitted) / any parity / 1 or 2 stop bits
- RTU: 8 bit binary coding / 8 bits per character (8 transmitted) / any parity / 1 or 2 stop bits

RTU sends data in 8-bit binary characters. ASCII firsts divides each RTU character into two 4-bit parts (high order and low order) and then represents them by their hexadecimal equivalent. The ASCII characters representing the hexadecimal characters are used to construct the message thus using twice as many characters as RTU mode. Additionally, RTU message characters are transmitted in a continuous stream, whereas ASCII can have breaks of up to one second between characters.

The following is a guideline for configuring a serial port interface from the control to a communication device.

	Modbus RTU
BAUD	10 (38400)
BITS	2 (RTU-8 bits)
STOP	1 (1 stop)
PARITY	1 (none)
MODE	1 (line)
FLOW	1 (off)
ECHO	1 (off)
ENDLINE	3 (crlf)
IGNCR	1 (off)

Table 8-4. Serial Port Interface Guideline

Serial (Modbus) Software Configuration (GAP)

See the next section for specific configuration examples for Automation Direct applications.

After the GAP application has been completed, it must be compiled and downloaded to the AtlasSC control using AppManager. Once the GAP application is started on the AtlasSC, the Serial Modbus module will automatically initialize and start the Modbus Network.



The timing of the serial scan rate will depend on the number of groups (heads) and the number of nodes (I/O modules) in the network and will be independent of the rate group structure.

Distributed I/O Example

This section documents the hardware setup and software configuration for one manufacturer of Distributed I/O modules. Since there are numerous options available from numerous manufacturers, Woodward chose Automation Direct Terminator I/O hardware as a system representative. There are other good high-end and low-cost manufacturers of Distributed I/O systems, and therefore this example should not be taken as a Woodward endorsement. The I/O configurations were limited to specific I/O modules and a limited application of the possible options available with these modules.

It is the responsibility of the end user to review the options available from each distributed I/O supplier and derive the necessary software configurations needed to support their specific applications. Woodward makes no expressed or implied statement of suitability for Automation Direct or any other supplier of Distributed I/O systems. We hope that this specific example of I/O module configurations can be used as a foundation for building a majority of the AtlasSC control applications where Distributed I/O is needed. Ease in applying the Woodward software interface may differ from vendor to vendor. Due to limited resources, Woodward can only supply limited technical support for those applications using other vendors or part numbers not shown in these examples. If extensive support is required, contact Woodward for setting up an application development contract.

At this writing, Automation Direct has Modbus Interface Modules that are compatible with the AtlasSC serial interface.

The following example will demonstrate a systematic process to use in generating a distributed I/O network.



Implementing a Distributed I/O Network

- 1. Determine the number and type of I/O channels needed.
- 2. Select the best manufacturer of I/O modules that meet the network and I/O requirements.
- 3. Lay out the hardware configuration (how the modules will be arranged and wired).
- 4. Create the Modbus addressing configuration.
- 5. Generate an address spreadsheet to track address numbers for Reads and Writes based on the specific module memory maps.
- 6. Review the published documentation on the I/O modules and select the options required.
- 7. Generate the GAP I/O files.
- 8. Wire control and Distributed I/O network.
- 9. Download the GAP into the control.
- 10. Run application and verify functionality.

Automation Direct Terminator I/O Modbus Configuration

The Terminator I/O is a modular system which combines the functions of terminal blocks and I/O modules for distributed I/O. Each Terminator I/O system has the following components: a Power Supply, a Base Controller, and one or more I/O Module(s). Terminator I/O systems can contain up to 16 I/O modules per slave (node). Each slave (node) system can be divided into one row of base I/O plus two rows of local expansion I/O using a base expansion cable.

In this example, one T1K-MODBUS adapter and two power supplies from Automation Direct are interfaced with seven terminal base units with installed Terminator I/O modules, forming a Terminator I/O system.

The T1K-MODBUS is a Network Interface Adapter that communicates between the Terminator I/O modules backplane and the AtlasSC/Master across a serial RS-232 connection. The T1K-MODBUS module is a slave device to the AtlasSC control, and is a master controller of the Terminator I/O modules. The I/O data exchange occurs as follows. Output data is sent from the AtlasSC across the RS-232 connection to the T1K-MODBUS adapter. The network interface adapter then automatically transfers the data across the Terminator I/O backplane to the output modules. Inputs from the input modules are collected by the network interface adapter via the backplane and sent across the RS-232 connection to the AtlasSC control.

In order to simplify the nomenclature used in creating the GAP application, certain naming conventions were changed with respect to the manufacturer's naming convention. In this example, Woodward refers to the node address as the Group address. The T1K-MODBUS Group address, (Node address), is set by two rotary switches located on the front of the T1K-MODBUS module. In this example the address is set to 01.

The Automation Direct Terminator I/O system requires DIN rail mounted power supplies to power the individual I/O module over the Terminator I/O back plane. In this example, a 120 Vac unit (T1K_01AC) was used to power the Discrete I/O modules and a 24Vdc unit (T1K-01DC was used to power the Analog I/O modules.

In this example, the power supplies were also used to generate the needed current for the externa`l power requirements of the circuits. See Figure 8-1 for Automation Direct Terminator I/O configuration layout.



Figure 8-1. Automation Direct Terminator I/O Configuration

Address Spreadsheet

The I/O map for a module is divided into read words and write words. Read words consist of input and status words, and write words consist of output and configuration words. The number of read words or write words can be 0 or more.

To keep track of the addressing for all of the reads and writes, an address spreadsheet is very useful. This address spreadsheet can be derived from memory maps in the I/O modules User Manual and the network interface module (head) T1K-MODBUS User Manual. These memory maps define which addresses to allocate within the Address Spreadsheet. Not all of the addresses allocated to each module are written to or read from. Some addresses are either not used or reserved. See specific I/O module memory maps for detail functionality used to define the GAP reads and writes. The Automation Direct Terminator I/O system addresses its discrete I/O by bits and analog I/O by words. See Table 8-1 for the address spreadsheet for this example.

Nomenclature

When creating a new GAP application, it is important to establish a well organized block naming convention up front. Once done, it is easier to find specific functions and I/O within a large GAP application.

GAP Application

In order to communicate between the Atlas serial RS-232 port and the T1K-MODBUS, a MOD_PORT and MODBUS_M block must be defined in GAP. The MOD_PORT block defines the serial communications baud rate, stop, parity, and type of interface (RS-232, -422, -485). The MODBUS_M block defines the addressing for Modbus communications between the Atlas and the distributed I/O. Based on the Address Spreadsheet, this block can be customized to read and write to appropriate addresses. See Figure 8-2 for MODBUS_M/MOD_PORT setup example.



Figure 8-2. MODBUS_M Block Example

The E_ERR_1_x output field on the MODBUS_M block is used to display Exception Error on PORT_1 per RPTx. It latches TRUE when an exception error is detected. E_NUM_1_X (hidden) displays the error number. See Table 8-2 for exception errors.

Module	Byte #	Read Address Bits	Write Address Bits
T1K-MODBUS	0	None	None
T1K-08ND3	0	10000-10008	None
T1K-16ND3	0	10009-10016	None
	1	10017-10024	None
T1K-16TD1	0	None	00000-00008
	1	None	00009-00016
T1K-08TRS	0	None	00017-00024
Module	Word #	Read Addr. Words	Write Addr. Words
T1F-16AD-1	0	30001	None
	1	30002	
	2	30003	
	3	30004	
		-	
		-	
	29	30030	
	30	30031	
	31	30032	
T1F-16DA-1	0	None	40001
	1		40002
	2		40003
	3		40004
	29		40030
	30		40031
	31		40032
T1F-14THM	0	30033	None
	1	30034	
	2	30035	
	3	30036	
	29	30062	
	30	30063	
	31	30064	

Table 8-1.	Modbus	Word Address	Spreadsheet
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Use the following documents from the manufacturer to obtain the Memory Maps and related information.

Module	Document P/N
I/O modules	T1K-INST-M
Modbus Interface	T1K-MODBUS-M

CODE	NAME	MEANING	
The fo	The following are generated by the slave.		
00	NO ERROR	Error free	
01	ILLEGAL FUNCTION	Message function received is not an allowable action for addressed slave. (Unsupported or illegal function code).	
02	ILLEGAL DATA ADDRESS	Address referenced in data field is not an allowable address for the addressed slave location. (Master requested data which is not configured from slave).	
03	ILLEGAL DATA VALUEA	mount of data requested from slave was too large for slave to return in a single response.	
The fo	llowing are generated	l by the master.	
09	CHECKSUM ERROR	Error in checksum in message from slave. Can indicate link quality and/or noise problems.	
10	GARBLED MESSAGE	Data received from the slave, but is too short to be a valid Modbus message/response.	
20	UNSOLICITED RESPONSE	Unsolicited message received from slave.	
21	BAD FC IN RESPONSE	Slave returned a message with a different function code from the command sent.	
22	BAD ADD IN RESPONSE	Slave returned a message with a different address from the command sent.	
23	NO SLAVE RESPONSE	No response from slave.	
24	MOE/CODER ERROR	MOE/CODER error.	
25	INTERNAL SYS ERROR	Internal system error.	

Table 8-2. MODBUS_M Exception Errors

The L_ERR_1_x is used to display Link Error on PORT_1 for RPTx. It goes TRUE when the slave fails to answer a data request. If a response isn't received in TIME_OUT_x seconds, the request is retried. If a response isn't received in TIME_OUT_x seconds after the request is retried, then this output is set true.

While waiting for a response from one slave, the master is not communicating with the other slaves. So, if multiple slaves fail at the same time (i.e. broken cable), the L_ERR_1_x for the first slave attempted will occur at 2 x TIME_OUT_x. But, the second L_ERR_1_x won't go true until 2 x TIME_OUT_x after the first failure because no communications are attempted with that slave until after the first has timed out.

In this example, the TIK-MODBUS adapter is configured for the 584 / 984 Addressing mode (Dip switch 7). See Figure 8-3, for the full T1K-MODBUS adapter addressing table.

Mo	dbus		T1K–M	IODBUS	
Dat	атуре	Range (Decimal)	Poi	nts	Memory Type
		1 – 1024	1024		Discrete Output
,	,011	1025 – 9999	-		not supported
l.	mut	10001 – 11024	1024		Discrete Input
Ir	iput	11025 – 19999	-		not supported
Madhua	Data Tura		V Memo	ry Range	
Moabus	Data Type	Range (Decimal)	Words Channel (16– bit) (32– bit)		Memory Type
	Analog Input	30001 – 30128	128	64	Analog Input Register
	Input Register	30129 - 38999	-	-	not supported
Input Register	Bit Input Register	30201 – 30264	64	-	Discrete Input Bit Register
	Input Register	39129 - 39999	-	-	not supported
Analog outpu		40001 – 40128	128	64	Analog Output Register
	Hold Register	40129 - 40200	-	-	not supported
Hold Register	Bit Output Register	40201 – 40264	64		Discrete Output Bit Register
	Hold Register	40265 - 49000			not supported
	Hold Register	49001 - 49128	128	-	Special Register
	Hold Register	49129 - 49999	-	-	not supported

Figure 8-3. T1K-MODBUS Adapter Addressing Table

In the RPT window for the MODBUS_M block, the BR_F_CODE and AR_F_CODE fields specify the Boolean and Analog read Function Codes for Modbus. The Boolean Read Function Code input defines the Boolean read and write addresses as follows:

1 = 00001–0FFFF Input Coils (Boolean READ/WRITE)

2 = 10001–1FFFF Input Status (Boolean READ ONLY)

The Analog Read Function Code input defines the Analog read and write addresses as follows: 3 = 40001–4FFFF Holding Registers (Analog READ/WRITE) 4 = 30001–3FFFF Input Registers (Analog READ ONLY)

By setting the BR_F_CODE to 2 and the AR_F_CODE field to 4 (defaults), the MODBUS_M block will access the following addresses as specified by the 584/984 Addressing mode of the T1K-MODBUS adapter:

1–1024 Discrete Outputs 10001–11024 Discrete Inputs 30001–30128 Analog Input Registers 30201–30264 Bit Input Registers 40001–40128 Analog Output Registers 40201–40264 Bit Output Registers

The S_ADD_1 field must agree with the Group or Node address for the T1K-MODBUS adapter. In this example, the address is set to 01 which matches the two rotary switch settings on the adapter. See Figure 8-4 for example of MODBUS_M repeat fields.

hide	< S_ADD_1	1
hide	< S_IP_1_1	(0)
hide	< S_IP_2_1	(0)
hide	< S_IP_3_1	(0)
hide	< S_IP_4_1	(0)
hide	< S_PORT_1	(0)
show	< I_MOD_1	*FALSE
show	< E_RST_1	*FALSE
hide	< TIMEOUT_1	(1.5)
show	> E_NUM_1_1	
show	> E_ERR_1_1	
show	> L_ERR_1_1	
hide	> E_NUM_2_1	
hide	> E_ERR_2_1	
hide	> L_ERR_2_1	
hide	< BW_ADD_1	0
>>	RPTbw1	
hide	< BR_F_CODE_1	(2)
hide	< BR_ADD_1	0
>>	RPTbr1	
hide	< AR_F_CODE_1	[4]
hide	< AR_ADD_1	
>>	RPTar1	
hide	< AW_ADD_1	0
>>	RPTaw1	
,	,	,

Figure 8-4. MODBUS_M Block RPT Example

Within the MODBUS_M RPT window, there are four repeats (RPTbw1, RPTbr1, RPTar1, and RPTaw1). Each one of these RPT fields opens another window which displays the addresses assigned for that particular RPT. See the specific module sections below for memory map function assignment. Example for the T1K-08ND3 module bits (BR_V_1_1): since the starting address is 10000, the BR_F_CODE = 2. The BR_ADD_1 offset is 0. Therefore the T1K-08ND3 module bit (BR_V_1_1) has an address of 10000 + 0 + 1 = 10001. All of the addresses specified in the address spreadsheet are mapped into the MODBUS_M block by applying this formula. See Figures 8-5, 8-6, 8-7, and 8-8 for all read/write address windows. Figure 8-5 shows the write addresses for the discrete output modules (T1K-16TD1 and T1K-08TRS), addresses 00001 to 00024.

hide	< BW_C_1_1	16TD1 CH1	hide < BW_C_1_13 16TD1 CH13
hide	< BW_V_1_1	AD16TD1.BO_01.B_NAME	hide < BW_V_1_13 AD16TD1.B0_13.B_NAM
hide	< BW C 1 2	16TD1 CH2	hide < BW_C_1_14 16TD1 CH14
hide	< BW V 1 2	AD16TD1.BO 02.B NAME	hide < BW_V_1_14 AD16TD1.B0_14.B_NAM
hide	< BW C 1 3	16TD1 CH3	hide < BW_C_1_15 16TD1 CH15
hide	< BW V 1 3	AD16TD1.BO 03.B NAME	hide < BW_V_1_15 AD16TD1.B0_15.B_NAM
hide	KBW C 1 4	16TD1 CH4	hide < BW_C_1_16 16TD1 CH16
hide	< BW V 1 4	AD16TD1.BO 04.B NAME	hide < BW_V_1_16 AD16TD1.B0_16.B_NAM
hide	< BW C 1 5	16TD1 CH5	hide < BW_C_1_17 08TRS CH1
hide	< BW V 1 5	AD16TD1.BO 05.B NAME	hide < BW_V_1_17 AD08TRS.B0_1.B_NAMI
hide	KBW C 1 6	16TD1 CH6	hide < BW_C_1_18 08TRS CH2
hide	< BW V 1 6	AD16TD1.BO 06.B NAME	hide < BW_V_1_18 AD08TRS.BO_2.B_NAMI
hide	< BW C 1 7	16TD1 CH7	hide < BW_C_1_19 08TRS CH3
hide	KBW V 1 7	AD16TD1.BO 07.B NAME	hide < BW_V_1_19 AD08TRS.BO_3.B_NAMI
hide	< BW C 1 8	16TD1 CH8	hide < BW_C_1_20 08TRS CH4
hide	< BW V 1 8	AD16TD1.BO 08.B NAME	hide < BW_V_1_20 AD08TRS.BO_4.B_NAMI
hide	< BW C 1 9	16TD1 CH9	hide < BW_C_1_21 08TRS CH5
hide	< BW V 1 9	AD16TD1.BO 09.B NAME	hide < BW_V_1_21 AD08TRS.BO_5.B_NAMI
hide	< BW C 1 10	16TD1 CH10	hide < BW_C_1_22 08TRS CH6
hide	< BW V 1 10	AD16TD1.BO 10.B NAME	hide < BW_V_1_22 AD08TRS.BO_6.B_NAMI
hide	< BW C 1 11	16TD1 CH11	hide < BW_C_1_23 08TRS CH7
hide	< BW V 1 11	AD16TD1.BO 11.B NAME	hide < BW_V_1_23 AD08TRS.BO_7.B_NAMI
hide	< BW C 1 12	16TD1 CH12	hide < BW_C_1_24 08TRS CH8
hide	< BW_V_1_12	AD16TD1.B0_12.B_NAME	hide < BW_V_1_24 D08TRS.BO_8.B_NAMI

Figure 8-5. MODBUS_M Block Boolean Write RPT Example

IMPORTANT

Figure 8-6 shows all of the used read input addresses for Discrete Input modules (T1K-08ND3 and T1K-16ND3), addresses 10001 to 10024.

hide	< BR_C_1_1	08ND3 CH1	hide	< BR_C_1_9	16ND3 CH1	hide	< BR_C_1_17	16ND3 CH9
hide	> BR_V_1_1		hide	> BR_V_1_9		hide	> BR_V_1_17	
hide	< BR D 1 1	(FALSE)	hide	< BR_D_1_9	(FALSE)	hide	< BR_D_1_17	(FALSE)
hide	< BR C 1 2	08ND3 CH2	hide	< BR_C_1_10	16ND3 CH2	hide	< BR_C_1_18	16ND3 CH10
hide	> BR V 1 2		hide	> BR_V_1_10		hide	> BR_V_1_18	
hide	< BR D 1 2	(FALSE)	hide	< BR_D_1_10	(FALSE)	hide	< BR_D_1_18	(FALSE)
hide	< BR C 1 3	08ND3 CH3	hide	< BR_C_1_11	16ND3 CH3	hide	< BR_C_1_19	16ND3 CH11
hide	> BR V 1 3	1	hide	> BR_V_1_11		hide	> BR_V_1_19	
hide	< BB D 1 3	(FALSE)	hide	< BR_D_1_11	(FALSE)	hide	< BR D 1 19	(FALSE)
hide	< BB C 1 4	08ND3 CH4	hide	< BR_C_1_12	16ND3 CH4	hide	< BR C 1 20	16ND3 CH12
hide	$> BB \vee 1 4$		hide	> BR_V_1_12		hide	> BR V 1 20	
hide	KBB D 1 4	(EALSE)	hide	< BR D 1 12	(FALSE)	hide	< BR D 1 20	(FALSE)
hide	KBB C 1 5	08ND3 CH5	hide	< BR C 1 13	16ND3 CH5	hide	< BR C 1 21	16ND3 CH13
hide	> BR V 1 5		hide	> BR ¥ 1 13		hide	> BR V 1 21	
hide	2 BR D 1 5	(EALSE)	hide	< BR D 1 13	(FALSE)	hide	< BR D 1 21	(FALSE)
hide			hide	< BR C 1 14	16ND3 CH6	hide	< BR C 1 22	16ND3 CH14
hide			hide	> BR V 1 14		hide	> BR V 1 22	
hide			hide	< BR D 1 14	(FALSE)	hide	< BR D 1 22	(FALSE)
hide			hide	< BR C 1 15	16ND3 CH7	hide	< BR C 1 23	16ND3 CH15
hide			hide	> BB V 1 15		hide	> BB V 1 23	
	2 DR_V_1_7	(GALOD)	hide	< BB D 1 15	(FALSE)	hide	(BB D 1 23	(EALSE)
l mae			hide	< BB_C_1_16	16ND3 CH8	hide	(BB C 1 24	16ND3 CH16
nide			hide	BR V 1 16		hide	NBR V 1 24	
hide	> BR_V_1_8		hide		(EALSE)	hide		(EALSE)
hide	< BR_D_1_8	[[FALSE]	mac		(I ALOL)	mue		ILL WEAR

Figure 8-6. MODBUS M Block Boolean Read RPT Example

Figure 8-7 shows all of the used read input addresses for analog Input modules (T1F-16AD-1 and T1F-14THM), addresses 30001 to 30060.

The Automation Direct analog modules use two address words per channel. The first word is used, and the second is not used. The unused address must be accounted for when addressing the modules and therefore requires each channel to increment by two addresses. Example: 2X16 addresses for the T1F-AD-1 module + 2X14 addresses for the T1F-14THM module = 60 addresses total.

$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	bide CAB C 1 1	• 164D-1 CH1	hide < AR_C_1_11	16AD-1 CH6	hide < AR C 1 21	16AD-1 CH11	hide CAR C 1 31	16AD-1 CH16	hide < AR C 1 41	14THM CH5	hide CAR C 1 51	14THM CH10
$ \begin{array}{c} \mbox{line}{(a, a, b, 1, 1)} \\ \mbox{line}{(a, a, b, 1, 2)} \\ \mbox{line}{(a, a, b, 1$	hide > AB V 1 1	- I one i oni	hide > AR_V_1_11		hide > AR_V_1_21		hide > AR V 1 31		hide > AR V 1 41		hide > AR V 1 51	
$ \begin{array}{c} \mbox{he} c \ AR \ D = 1 \\ \mbox{he} c \$	hide CAB M 1 1	n.m	hide < AR_M_1_11	(1.0)	hide < AR_M_1_21	[1.0]	hide < AR M 1 31	(1.0)	hide CAR M 1 41	11.01	hide < AR M 1 51	(1.0)
$ \begin{array}{c} \mbox{he} c \ c \ c \ c \ c \ c \ c \ c \ c \ c $	hide CAR D 1 1	10.01	hide < AR_D_1_11	[0.0]	hide < AR_D_1_21	(0.0)	hide < AR D 1_31	(0.0)	hide < AR D 1 41	(0.0)	hide < AR_D_1_51	[0.0]
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide (AB C 1 2	(0.0)	hide < AR_C_1_12		hide < AR_C_1_22		hide < AR C 1 32		hide < AR C 1 42	1	hide < AR C 1 52	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide AR V 1 2		hide > AR_V_1_12		hide > AR V 1 22		hide > AB V 1 32		hide > AR V 1 42		hide > AB V 1 52	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide (AR M 1.2	n m	hide < AR_M_1_12	(1.0)	hide < AR M 1 22	(1.0)	hide CAR M 1 32	r1.01	hide CAR M 1 42	r1.01	hide < AR M 1 52	r1.01
Inde CAR_D_1_3 Inde CAR_D_1_2 Inde CAR_D_1_2 Inde CAR_D_1_2 Inde CAR_D_1_2 Inde CAR_D_1_3	hide CAR D 1 2	0.0	hide < AR D 1 12	(0.0)	hide < AR D 1 22	(0.0)	hide (AR D 1 32	10.01	bide < AB D 1 42	10.01	hide (AR D 1 52	10.01
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	hide CAR C 1 3	16AD-1 CH2	hide < AR C 1 13	16AD-1 CH7	hide CAR C 1 23	16AD-1 CH12	hide < AB C 1 33	14THM CH1	hide CAB C 1 43	14THM CH6	bide < AB C 1 53	14THM CH11
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide AB V 1 3	Ture I cite	hide > AR_V_1_13		hide > AR V 1 23		hide > AR V 1 33		hide > AR V 1 43		hide > AR V 1 53	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide (AD M 1.2	a.m.	hide CAR M 1 13	11.01	hide CAR M 1 23	r1.01	hide CAB M 1 33	rt.m	bide CAB M 1 43	11.00	hide CAR M 1 53	rt.m
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAP D 1 3	(0.0)	hide < AR D 1 13	(0.0)	hide < AR D 1 23	10.01	hide CAR D 1 33	(0.0)	bide CAB D 1 43	0.0	hide CAR D 1 53	0.0
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR C 1 A	(0.0)	hide < AR C 1 14		hide CAR C 1 24		hide < AB C 1 34	(****)	hide CAB C 1 44	(0.0)	bide < AB C 1 54	1000
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	hide \AR_C_1_4		hide > AR V 1 14		hide > AR V 1 24	-	hide > AB V 1 34		hide DAR V 1 44		hide > AB V 1 54	
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide (AD M 1 4	a	hide CAR M 1 14	(1.0)	hide CAB M 1 24	n.n	hide CAR M 1 34	r1.01	bide CAB M 1 44	ci.m	hide CAR M 1 54	n.m
$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	hide CAP D 1 4	(0.0)	hide < AR D 1 14	10.01	hide < AR D 1 24	10.01	hide CAR D 1 34	0.0	hide CAB D 1 44	0.0	hide CAB D 1 54	0.0
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR_0_1_4	16AD-1 CH2	hide CAR C 1 15	16AD-1 CH8	hide CAR C 1 25	16AD-1 CH13	hide CAR C 1 35	14THM CH2	hide CAR C 1 45	14THM CH7	hide CAR C 1 55	14THM CH12
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide \AR_C_1_5	TOAD-T CHS	hide > AR V 1 15		hide > AR V 1 25		hide > AB V 1 35		hide DAR V 1 45		hide > AB V 1 55	1 TITLE
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAP M 1.5	a m	hide < AR M 1 15	r1.01	hide KAB M 1 25	n.m	hide CAR M 1 35	r1.00	hide CAR M 1 45	0.0	hide CAB M 1 55	r1.00
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR_M_1_5	(1.0)	hide CAR D 1 15	(0.0)	hide CAB D 1 25	0.0	hide CAP D 1 35	(0.0)	hide CAR_M_1_45	(0.0)	hide CAP D 1 55	(0.0)
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR_D_1_5	[0.0]	hide CAB C 1 16	()	hide CAB C 1 26		hide CAR C 1 36	(0.0)	hide (AR_D_1_45	[0.0]	hide CAR C 1 56	(0.0)
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR_C_1_6		hide > AR V 1 16		hide DAR V 1 26		hide \AR V 1 36				hide \AR V 1 56	
$ \begin{array}{c} \mbox{nu} = (aAU_{M} = 0 \\ \mbox{i} $		(1 m)	hide CAB M 1 16	n.m	hide CAB M 1 26	n.m	hide (AR M 1 36	1 m	hide CAP M 1 46	0.00	hide (AR M 1 56	11 m
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	hide CAR_M_1_6	(1.0)	hide CAB D 1 16	0.0	hide CAB D 1 26	0.0	hide (AR D 1 36	0.0	hide CAR D 1 46	0.0	hide CAP D 1 56	0.0
$ \begin{array}{c} hide \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	hide CAR_D_1_6	[0.0]	hide CAR C 1 17	16AD-1 CH9	hide CAB C 1 27	16AD-1 CH14	hide CAR C 1 37	14THM CH3	hide (AR_D_1_40	14THM CHR	hide CAR C 1 57	14THM CH13
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		TOAD+1 CH4	hide > AB V 1 17		hide > AB V 1 27	Toras T onti T	hide \ARV1.37		bide AD V 1 47	Turring Crite	hide \AR V 1 57	
$ \begin{array}{c} hide \ (AR_D_1=7 \\ hide \ (AR_D_1=7 \ hide $		(1 m)	hide CAR M 1 17	r1.00	hide CAB M 1 27	n.m	hide CAR M 1 37	1 m	hide CAP M 1 47	0.0	hide CAR M 1 57	11 m
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR_M_1_7	(1.0)	hide CAB D 1 17	0.0	hide CAB D 1 27	0.0	hide (AR D 1 37		hide CAR D 1 47	10.0	hide (AP D 1 57	0.0
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	hide CAR_D_1_7	[0.0]	hide CAR C 1 18	(0.0)	hide CAB C 1 28	(0.0)	hide CAR C 1 38	[0.0]	hide CAR_D_1_47	[[0.0]	hide CAR C 1 58	[0.0]
Inde CAR_M_1 In			hide > AB V 1 18		hide > AB V 1 28		hide \ARV 1 38		hide XAR V 1 49		hide \AR V 1 58	
Inde CAT_D_1 Itide CAT_D_1 <thcat_d_1< th=""> <thcat_d_1< th=""> <thcat_d_1< <="" td=""><td>hide AR_V_1_6</td><td></td><td>hide CAB M 1 18</td><td>rt.m</td><td>hide CAR M 1 28</td><td>n.m</td><td>hide CAR M 1 38</td><td>r. m</td><td>hide CAD M 1 49</td><td>10.00</td><td>hide CAP M 1 50</td><td>11 m</td></thcat_d_1<></thcat_d_1<></thcat_d_1<>	hide AR_V_1_6		hide CAB M 1 18	rt.m	hide CAR M 1 28	n.m	hide CAR M 1 38	r. m	hide CAD M 1 49	10.00	hide CAP M 1 50	11 m
	hide CAR_M_I_0	(1.0)	hide CAR D 1 18	(0.0)	hide CAB D 1 28	0.0	hide CAR D 1 38	(0.0)	hide (AR_M_1_40	(1.0)	hide (AP D 1 58	(1.0)
LEAL FAILER A DECEMBER AND LEAD FOR THE	hide CAR_D_1_0	10.0	hide CAB C 1 19	16AD-1 CH10	hide CAB C 1 29	16AD-1 CH15	hide CAR C 1 39	14THM CH4	bide CAR_D_1_40	1 ATHIN CHO	hide CAR C 1 59	1 ATHM CH14
	hide CAR_C_1_9	TEAD-T CH5	hide AR V 1 19	Turney I Cittle	hide AP V 1 29	Turney T GITTS	hide ARLC_1_33	TATTIM CHA	hide CAR_C_1_43	I FATTIM CHS	hide \AR_C_1_55	Tarrine Crita
	hide > AR_V_1_9		hide CAR M 1 19	r1 m	Nida / AD M 1 29	a	hide (AD M 1 30	a	hide AR_V_1_49	- u m	hide / AD M 1 E0	
	hide CAR_M_1_9	[1.0]	hide CAB D 1 19	10.01	hide CAB D 1 29	0.0	hide CAP D 1 39	0.0	hide (AP D 1 49	(0.0)	hide CAR D 1 59	0.0
	mae CAR_D_1_9	[0:0]	bide CAP C 1 20	[3:0]	hide (AR C 1 30	13.01	hide CAR_D_1_39	[0.0]		[0.0]	hide (AR_0_1_59	[0.0]
	hide CAR_C_1_10		hide AP V 1 20		bide AP V 1 30		hide AD V 1 40		hide CAR_C_1_50		hide VAR_C_1_60	
	hide > AR_V_1_10		hide CAR M 1 20	c1.00	bide (AP M 1 30	0.0	hide CAD M 1 40	(1 m)	hide AR_V_1_50	11.00	hide CAD M 1 50	0.00
	hide CAR_M_1_10	[1.0]	bide CAB D 1 20	(0.0)	hide CAB D 1 30	(0.0)	hide CAR D 1 40	(1.0)	hide CAR_M_1_50	(0.0)	hide CAR_M_1_60	- (0.0)

Figure 8-7. MODBUS_M Block Analog Read RPT Example

Figure 8-8 shows all of the used write output addresses for the analog output module (T1F-16DA-1), addresses 40001 to 40032.



The Automation Direct analog modules use two address words per channel. The first word is used, and the second is not used. The unused address must be accounted for when addressing the modules and therefore requires each channel to increment by two addresses.

bide ZAW C 1 1	16DA-1 CH1	hide	< AW C 1 13	16DA-1 CH7	hide	< AW C 1 25	16DA-1 CH13
hide (AW V 1 1		hide	< AW V 1 13	AD16DA-1.AO 07.CURVE 2D	hide	< AW V 1 25	AD16DA-1.AO 13.CURVE 2D
hide (AW M 1 1	1 0	hide	AW M 1 13	r1.m	hide	(AW M 1 25	n
hide (AW C 1 2	15DA-1 MODULE CONTROL BYTE	hide	< AW C 1 14	(,	hide	< AW C 1 26	,
hide (AW V 1 2	AD16DA-1 MODCTL BYTE OUT 1	hide	< AW V 1 14		hide	< AW V 1 26	
hide (AW M 1.2	n m	hide	< AW M 1 14	(1.0)	hide	< AW M 1 26	r1.01
hide (AW C 1 3	16DA-1 CH2	hide	< AW C 1 15	16DA-1 CH8	hide	(AW C 1 27	16DA-1 CH14
hide (AW V 1 3		hide	< AW V 1 15	AD16DA-1.AO 08.CURVE 2D	hide	< AW V 1 27	AD16DA-1.AO 14.CURVE 2D
hide (AW M 1 3	1.0	hide	< AW M 1 15	(1.0)	hide	(AW M 1 27	(1.0)
hide (AW C 1 4	(1.0)	hide	KAW C 1 16	, , , , , , , , , , , , , , , , , , ,	hide	< AW C 1 28	
hide (AW V 1 4		hide	< AW V 1 16		hide	< AW V 1 28	
	rt m	hide	KAW M 1 16	(1.0)	hide	< AW M 1 28	r1.00
hide (AW C 1 5	16DA-1 CH3	hide	KAW C 1 17	16DA-1 CH9	hide	< AW C 1 29	16DA-1 CH15
hide (AW V 1 5	AD16DA-1 AO 03 CURVE 2D	hide	< AW V 1 17	AD16DA-1.AO 09.CURVE 2D	hide	< AW V 1 29	AD16DA-1.AO 15.CURVE 2D
hide < AW M 1 5		hide	< AW M 1 17	(1.0)	hide	< AW M 1 29	(1.0)
hide (AW C 1 6	(110)	hide	< AW C 1 18		hide	< AW C 1 30	. ,
hide < AW V 1 6		hide	< AW V 1 18		hide	< AW V 1 30	
hide (AW M 1.6	r1.m	hide	< AW M 1 18	[1.0]	hide	< AW M 1 30	(1.0)
hide < AW C 1 7 -	16DA-1 CH4	hide	< AW_C_1_19	16DA-1 CH10	hide	< AW C 1 31	16DA-1 CH16
hide < AW V 1 7	AD16DA-1.AO 04.CUBVE 2D	hide	< AW V 1 19	AD16DA-1.AO 10.CURVE 2D	hide	< AW V 1 31	AD16DA-1.AO 16.CURVE 2D
hide < AW M 1 7	(1.m	hide	< AW_M_1_19	[1.0]	hide	< AW M 1 31	[1.0]
hide < AW C 1 8	()	hide	< AW_C_1_20		hide	< AW_C 1 32	
hide < AW V 1 8		hide	< AW_V_1_20		hide	< AW_V_1_32	
hide < AW M 1 8	n.o	hide	< AW_M_1_20	(1.0)	hide	< AW M 1 32 👻	(1.0)
hide < AW C 1 9	16DA-1 CH5	hide	< AW_C_1_21	16DA-1 CH11	-	,	
hide < AW V 1 9	AD16DA-1.AO 05.CURVE 2D	hide	< AW_V_1_21	AD16DA-1.AO_11.CURVE_2D			
hide < AW M 1 9	(1.0)	hide	< AW_M_1_21	(1.0)			
hide < AW C 1 10	, í	hide	< AW_C_1_22				
hide < AW V 1 10		hide	< AW_V_1_22				
hide < AW M 1 10	(1.0)	hide	< AW_M_1_22	(1.0)			
hide < AW_C_1_11	16DA-1 CH6	hide	< AW_C_1_23	16DA-1 CH12			
hide < AW_V_1_11	AD16DA-1.AO_06.CURVE_2D	hide	< AW_V_1_23	AD16DA-1.AO_12.CURVE_2D			
hide < AW_M_1_11	(1.0)	hide	< AW_M_1_23	(1.0)			
hide < AW_C_1_12		hide	< AW_C_1_24				
hide < AW_V_1_12		hide	< AW_V_1_24				
hide < AW_M_1_12	(1.0)	hide	< AW_M_1_24	(1.0)			
	Eiguro 8	Q N/		M Block Applog W/	ito I	DDT Evon	nlo
	Figure o-	0. IV	00000	IVI DIUCK AHAIOY VVI	ILC I		ihie

Automation Direct Modbus Interface (T1K-MODBUS) Module



In this example, the T1K-MODBUS adapter was set up for RS-232 communications. The T1K-MODBUS RS-232 communications setup must be configured manually with DIP switches located on the Adapter. These DIP switches must agree with the settings specified in the MOD_PORT and MODBUS_M blocks. The MOD_PORT block uses 8 data bits per word. The settings for this example are shown in Figure 8-9, T1K-MODBUS Dip Switch Settings.





This configures the module for 38400 Baud rate (Sw1-3), default communication setting mode (Sw4), hold outputs off (Sw5), 9600 bps for RJ12 connection (Sw6), 584/984 Modbus slave addressing mode (Sw7), and disable the CTS pin on the RJ12 connector (Sw8). Because the default communications mode is selected, the RJ12 connection is not used. The default communications mode is set for 8 bit communications data, 1 start bit, 1 stop bit, odd parity, 1second communication timeout, and 0 ms RTS Delay time. The MOD_PORT GAP block settings must match these communication settings when configured.

The T1K-MODBUS adapter will support RS-232, RS-422, and RS-485 connections. In this example, the connection between the AtlasSC communications port (Comm 02) and the T1K-MODBUS adapter was wired as a RS-232 connection. See Figure 8-10 for an example of the RS-232 connection.



Figure 8-10. Automation Direct Modbus RS-232 Serial Connection

The Automation Direct T1K-MODBUS adapter doesn't reserve any read or write addresses for status words.



Automation Direct Discrete Input (Sink) (T1K-08ND3) Module



The T1K-08ND3 can be configured for sink or source detection with internal or external power. In this example, the unit is configured for sink mode with external power. This configuration is set up with jumpers located on the module. See Figure 8-11 for T1K-08ND3 Sink/External Power Configuration.



Figure 8-11. T1K-08ND3 Sink/External Power Configuration

See Figure 8-12 for example of T1K-08ND3 module wiring.

DIST Automation	RIBUTED I/O n Direct T1K-08ND	3
DISC	RETE INPUT	
	Input	Input 0
MBI.A1.A02.G1.N1_BI_01	Common	Com0-0
	Internal Supply Voltage	V0-0
	Input	Input 1
MBI.A1.A02.G1.N1 BI 02	Common	Com0-1
	Internal Supply Voltage	V0-1
	Input	Input 2
MBI.A1.A02.G1.N1 BI 03	Common	Com0-2
	Internal Supply Voltage	V0-2
	Input	Input 3
MBI.A1.A02.G1.N1 BI 04	Common	Com0-3
	Internal Supply Voltage	V0-3
	Input	Input 4
MBI.A1.A02.G1.N1_BI_05	Common	Com1-4
	Internal Supply Voltage	V1-4
-	Input	Input 5
MBI.A1.A02.G1.N1_BI_06	Common	Com1-5
	Internal Supply Voltage	V1-5
	Input	Input 6
MBI.A1.A02.G1.N1_BI_07	Common	Com1-6
	Internal Supply Voltage	V1-6
	Input	Input 7
MBI.A1.A02.G1.N1_BI_08	Common	Com1-7
	Internal Supply Voltage	V1-7



The T1K-08ND3 memory map indicates that there is one read byte (8 bits) address and no write addresses. See Figure 8-13 for memory map of Automation Direct T1K-08ND3 Discrete input module.

Memory Map of 8-Point Discrete Input Modules (T1K–08NA–1 and T1K–08ND3)											
Decimal Bit	07	06	05	04	03	02	01	00	Cine		
Octal Bit	07	06	05	04	03	02	01	00	Size		
	X7	X6	X5	X4	X3	X2	X1	X0	Read Byte 1		
	Write Byte 1										

Figure 8-13. T1K-08ND3 Memory Map

The Automation Direct T1K-08ND3 doesn't have any filter or other options to set, therefore there are no write addresses assigned. In this example, each bit is displayed using a B_NAME block. Each block is connected to the specific address in the MODBUS_M block RPT window shown above. See Figure 8-14 for example of T1K-08ND3 Read GAP blocks.





Automation Direct Discrete Input (Sink) (T1K-16ND3) Module



The T1K-16ND3 can be configured for sink or source detection with internal or external power. In this example, the unit is configured for sink mode with external power. This configuration is set up with jumpers located on the module. See Figure 8-15 for T1K-16ND3 Sink/External Power Configuration.



Figure 8-15. T1K-16ND3 Sink/External Power Configuration

See Figure 8-16 for example of T1K-16ND3 module wiring.

DIST Automatio	RIBUTED I/O n Direct T1K-16ND	03
DISC	RETE INPUT	
MBI.A1.A02.G1.N2_BI_01	Input Common Internal Supply Voltage	Input 0 Com0-0 V0-0
MBI.A1.A02.G1.N2_BI_02	Input Common Internal Supply Voltage	Input 1 Com0-1 V0-1
MBI.A1.A02.G1.N2_BI_03	Input Common Internal Supply Voltage	Input 2 Com0-2 V0-2
MBI.A1.A02.G1.N2_BI_04	Input Common Internal Supply Voltage	Input 3 Com0-3 V0-3
MBI.A1.A02.G1.N2_BI_05	Input Common Internal Supply Voltage	Input 4 Com1-4 V1-4
MBI.A1.A02.G1.N2_BI_06	Input Common Internal Supply Voltage	Input 5 Com1-5 V1-5
MBI.A1.A02.G1.N2_BI_07	Input Common Internal Supply Voltage	Input 6 Com1-6 V1-6
MBI.A1.A02.G1.N2_BI_08	Input Common Internal Supply Voltage	Input 7 Com1-7 V1-7
MBI.A1.A02.G1.N2_BI_09	Input Common Internal Supply Voltage	Input 8 Com2-8 V2-8
MBI.A1.A02.G1.N2_BI_10	Input Common Internal Supply Voltage	Com2-9 V2-9
MBI.A1.A02.G1.N2_BI_11	Input Common Internal Supply Voltage	Com2-10 V2-10
MBI.A1.A02.G1.N2_BI_12	Common Internal Supply Voltage	Com2-11 V2-11
MBI.A1.A02.G1.N2_BI_13	Common Internal Supply Voltage	Com3-12 V3-12
MBI.A1.A02.G1.N2_BI_14	Common Internal Supply Voltage	Com3-13 V3-13
MBI.A1.A02.G1.N2_BI_15	Common Internal Supply Voltage	Com3-14 V3-14
MBI.A1.A02.G1.N2_BI_16	Common	Com3-15



The T1K-16ND3 memory map indicates that there are two read byte (16 bits) addresses and no write addresses. See Figure 8-17 for memory map of Automation Direct T1K-16ND3 Discrete input module.

Memory Map of 16-Point Discrete Input Modules (T1K–16NA–1 and T1K–16ND3)											
Decimal Bit	07	06	05	04	03	02	01	00	Sizo		
Octal Bit	07	06	05	04	03	02	01	00	5128		
	X7	X6	X5	X4	X3	X2	X1	X0	Read Byte 1		
	X17	X16	X15	X14	X13	X12	X11	X10	Read Byte 2		
	Write Byte 1										

Figure 8-17. T1K-16ND3 Memory Map

The Automation Direct T1K-16ND3 doesn't have any filter or other options to set, therefore there are no write addresses assigned. In this example, each bit is displayed using a B_NAME block. Each block is connected to the specific address in the MODBUS_M block RPT window shown above. See Figure 8-18 for example of T1K-16ND3 Read GAP blocks.





Automation Direct Discrete Output (Sink) (T1K-16TD1) Module



In this example the T1K-16ND3 was connected to a Woodward 16 channel relay module. See Figure 8-19 for example of T1K-16ND3 module wiring.



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Figure 8-19. T1K-16ND3 Module to Relay Module Wiring (example)

The T1K-16TD1 memory map indicates that there are no read byte (8 bits) addresses and two write byte (16 bits) addresses. See Figure 8-20 for memory map of Automation Direct T1K-16TD1 Discrete Output module.

Memory Map of 16-Point Discrete Input Modules (T1K–16TA, T1K–16TD1, T1K–16TD2 and T1K–16TR)											
Decimal Bit	07	06	05	04	03	02	01	00	Sizo		
Octal Bit	07	06	05	04	03	02	01	00	5126		
		Read Byte 1									
	Y7	Y6	Y5	Y4	Y3	Y2	Y1	Y0	Write Byte 1		
	Y17	Y16	Y15	Y14	Y13	Y12	Y11	Y10	Write Byte 2		

Figure 8-20. T1K-16TD1 Module Memory Map

The Automation Direct T1K-16TD1 module doesn't have any status words to read, therefore there are no read addresses assigned. In this example, each bit is written using a B_NAME block. Each block is connected to the specific address in the MODBUS_M block RPT window shown above. See Figure 8-21 for example of T1K-16TD1 write GAP blocks.







Automation Direct Relay Output (T1K-08TRS) Module



See Figure 8-22 for example of T1K-08TRS module wiring.

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The T1K-08TRS memory map indicates that there are no read byte addresses and one write byte (8 bits) address. See Figure 8-23 for memory map of Automation Direct T1K-08TRS Relay Output module.

Memory Map of 8-Point Discrete Output Modules (T1K–08TA, T1K–08TD1 and T1K–08TR)										
Decimal Bit	07	06	05	04	03	02	01	00	Sizo	
Octal Bit	07	06	05	04	03	02	01	00	Size	
		Read Byte 1								
	Y7	Y6	Y5	Y4	Y3	Y2	Y1	Y0	Write Byte 1	

Figure 8-23. T1K-08TRS Module Memory Map

The Automation Direct T1K-08TRS module doesn't have any status words to read, therefore there are no read addresses assigned. In this example, each bit is written using a B_NAME block. Each block is connected to the specific address in the MODBUS_M block RPT window shown above. See Figure 8-24 for example of T1K-08TRS write GAP blocks.





(example)

Automation Direct Analog Input (T1F-16AD-1) Module



In this example, the T1F-16AD-1 is wired with loop powered transducers. See Figure 8-25 for example of T1F-16AD-1 module wiring.

DISTRIBUTED I/O Automation Direct T1F-16AD-1					
ANALOG INPUT					\bigcirc
MBI.A1.A02.G1.N5_AI_01	Input	Input 0		1	- (4-20mA) + Source)
	Common	Com0-0			\bigcirc
	External Supply Voltage	V0-0		2	- (1 20m) +
MBI.A1.A02.G1.N5_AI_02	Input	Input 1		3	Source
	Common	Com0-1			\leq
	External Supply Voltage	V0-1			- (4-20mA) +
MBI.A1.A02.G1.N5_AI_03	Input	Input 2			Source
	Common	Comu-2			~
	External Supply Voltage	V0-2			- (4-20mA) +
MBI.A1.A02.G1.N5_AI_04	Input	Input 3			Source
	Common	Comu-3			\sim
	External Supply Voltage	VU-3			- (4-20mA) +
MBI.A1.A02.G1.N5_AI_05 MBI.A1.A02.G1.N5_AI_06	Common	Com1.4			Source
	External Supply Voltage	V1-4			ě
	Input	Input 5			- (4-20mA) +
	Common	Com1-5			Source
	External Supply Voltage	V1-5			\sim
MBI.A1.A02.G1.N5_AI_07	Input	Input 6		13	- (4-20mA +
	Common	Com1-6			Source
	External Supply Voltage	V1-6			Ō
MBI.A1.A02.G1.N5_AI_08	Input	Input 7		15	- (4-20mA) +
	Common	Com1-7			Source
	External Supply Voltage	V1-7			\frown
MBI.A1.A02.G1.N5_AI_09	Input	Input 8		17	- (4-20mA) +
	Common	Com2-8			\bigcirc
	Common Supply Voltage	V2-8			
MBI.A1.A02.G1.N5_AI_10	Input	Input 9		19	- (4-20mA)
	Common	Com2-9			\bigcirc
	Common Supply Voltage	V2-9			+
MBI.A1.A02.G1.N5_AI_11	Input	Input 10		21	Source)
	Common	Com2-10			\bigcirc
	Common Supply Voltage	V2-10		_	- (4-20mA) +
MBI.A1.A02.G1.N5_AI_12	Input	Input 11		23	Source
	Common	Com2-11			\sim
	Common Supply Voltage	V2-11			- (4-20mA) +
MBI.A1.A02.G1.N5_AI_13	Input	Input 12		25	Source
	Common	V2 12			\sim
MBI.A1.A02.G1.N5_AI_14	Common Supply Voltage	VJ-12		27	- (4-20mA) +
	Common	Com3-12			Source
	Common Supply Voltage	V3-13			$\widetilde{\sim}$
MBI.A1.A02.G1.N5_AI_15	Input	Input 14		20	- (4-20mA +
	Common	Com3-14			Source
	Common Supply Voltage	V3-14			$\tilde{\frown}$
MBI.A1.A02.G1.N5_AI_16	Input	Input 15		31	- (4-20mA) +
	Common	Com3-15			Source
	Common Supply Voltage	V3-15		► To +24	VDC -
	Letter and a second sec	10 10		To 240	COM

Figure 8-25. T1F-16AD-1 Module Wiring (example)
The T1F-16AD-1 memory map indicates that there are 64 read byte (32 words) addresses and no write byte addresses. See Figure 8-26 for memory map of Automation Direct T1F-16AD-1 Analog Input module.

Mem	Memory Map of 16-Channel Analog Input Module						Module	Decimal Bit	07	06	05	04	0	3 0	2	01	00	Sizo			
Device al Dis	07	00		() जन्म	11-	-16A	U)	04	00		Octal Bit	07	06	05	04	0	3 0	2	01	00	5128
Decimal Bit	07	06	Ľ	10	94	03	02	01	00	Size			Ar	halog	Valu	Je (Chan	nel	9		Read Byte 33
Octal Bit	07	06	()5)4	03	02	01	00				Ar	nalog	Valu	le (Chan	nel	9		Read Byte 34
		AI	na	log V	alu	e Ch	anne	11		Read Byte 1					not	use	əd				Byte35
		A	na	log V	alu	e Ch	anne	1		Read Byte 2			re	eserv	ed fo	or fi	uture	us	e		Byte36
				r	ot ı	JSed				Byte3			Analog Value Channel 10						Read Byte 37		
		r	es	erve	1 10	r futi	ure us	50		Byte4			An	alog	Valu	еC	Chanr	nel	10		Read Byte 38
		A	na	log V	alu	e Ch	anne	12		Read Byte 5			not used							Byte39	
		Analog Value Channel 2					anne	12		Read Byte 6			reserved for future use						Byte40		
				r	ot ı	Jsed				Byte7			Analog Value Channel 11							Read Byte 41	
		reserved for future use Analog Value Channel 3						5e		Byte8			An	alog	Valu	еC	chanr	nel	11		Read Byte 42
		A	na	log \	alu	e Ch	anne	13		Read Byte 9					not	use	əd				Byte43
		Analog Value Channel 3					anne	13		Read Byte 10			re	eserv	ed fo	or fi	uture	us	e		Byte44
	not used								Byte11			An	alog	Valu	еC	Chanr	nel	12		Read Byte 45	
	reserved for future use					ure us	5e		Byte12		Analog Value Channel 12						Read Byte 46				
		A	na	log \	alu	e Ch	anne	4		Read Byte 13					not	use	əd	_		_	Byte47
		A	na	log \	alu	e Ch	anne	14		Read Byte 14			re	eserv	ed fo	or fi	uture	us	e	_	Byte48
				r	ot ı	Jsed				Byte15		Analog Value Channel 13						Read Byte 49			
		r	es	erve	i fo	r futi	ure us	50		Byte16		Analog Value Channel 13						Read Byte 50			
		A	na	log \	alu	e Ch	anne	15		Read Byte 17		not used						Byte51			
		A	na	log \	alu	e Ch	anne	15		Read Byte 18			re	eserv	ed fo	or fi	uture	us	e		Byte52
				r	ot ı	Jsed				Byte19			An	alog	Valu	еC	Chanr	nel	14		Read Byte 53
		r	es	erve	i fo	r futi	ure us	se		Byte20			An	alog	Valu	еC	Chanr	nel	14	_	Read Byte 54
		A	na	log \	alu	e Ch	anne	16		Read Byte 21					not	use	əd	_			Byte55
		A	na	log \	alu	e Ch	anne	16		Read Byte 22			re	eserv	ed fo	or fi	uture	us	e		Byte56
				r	ot ı	Jsed				Byte23			An	alog	Valu	еC	hanr	nel	15		Read Byte 57
		n	es	erve	i fo	r futi	ure us	50		Byte24			An	alog	Valu	еC	hanr	nel	15		Read Byte 58
		A	na	log V	alu	e Ch	anne	17		Read Byte 25				-	not	use	əd	_			Byte59
		A	na	log \	alu	e Ch	anne	17		Read Byte 26			re	eserv	ed fo	or fi	uture	us	e		Byte60
	not used								Byte27		Analog Value Channel 16						Read Byte 61				
	reserved for future use					ure us	se		Byte28		Analog Value Channel 16						Read Byte 62				
	Analog Value Channel 8					Read Byte 29		not used						Byte63							
	Analog Value Channel 8						Read Byte 30		reserved for future use						Byte64						
	not used						Byte31		Not Used						Write Byte 1						
	reserved for future use						ure us	5e		Byte32	L							_			

Figure 8-26. T1F-16AD-1 Analog Input Module Memory Map

To scale the analog input for the T1F-16AD-1 module, CURVE_2D blocks were used. For the GAP block to convert the raw counts read from the T1F-16AD-1 module to engineering units, the relationship between raw counts and engineering units is specified in the CURVE_2D block. The raw count to milliamp relationship can be found in the Automation Direct manual T1K-INST-M. See Figure 8-27 for Current Input Module Resolution. The charts specify counts in decimal form. In this example, the 0-20mA scaling graph was used. The CURVE_2D block can be scaled to output any engineering units desired. In this example, the CURVE_2D block was scaled to output 0-20mA when 0-20 mA is seen by the module. The X_1 field specifies the module output counts when the input is at 20 mA. The Y_1 field specifies the block output in milliamps when the input counts are 0. The Y_2 field specifies the block output in milliamps when the input is at maximum counts (8191). The CURVE_2D X_1 and Y_1 fields have been set up as tunable so that the block can be set for a 4-20mA scale if desired. See Figure 8-28 for example of T1F-16AD-1 GAP CURVE_2D scaling blocks.



Figure 8-27. T1F-16AD-1 Current Input Module Resolution

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Since each channel has two read words, each block address is incremented by two, starting at 30001 and going to 30031.



Figure 8-28. T1F-16AD-1 GAP CURVE_2D Scaling Blocks (example)

Automation Direct Analog Output (T1F-16DA-1) Module



See Figure 8-29 for example of T1F-16DA-1 module wiring.

DIST Automatior	RIBUTED I/O Direct T1F-16DA-	1			
ANAI	OG OUTPUT				
	Output	Output 0			- (4-20mA) +
MBI.A1.A02.G1.N6_AO_01	Common	Com0-0			Load
	External Supply Voltage	V0-0		2	<u> </u>
	Output	Output 1		3	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_02	Common	Com0-1			LOad
	External Supply Voltage	V0-1			\frown .
	Output	Output 2		5	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_03	Common	Com0-2			Load
	External Supply Voltage	V0-2			\frown
	Output	Output 3		7	- (4-20mA) +
MBI.A1.A02.G1.N6 AO 04	Common	Com0-3			LOBO
	External Supply Voltage	V0-3			
	Output	Output 4			- (4-20mA) +
MBI.A1.A02.G1.N6_AO_05	Common	Com1-4			Load
	External Supply Voltage	V1-4			\frown
	Output	Output 5			- (4-20mA +
MBI.A1.A02.G1.N6_AO_06	Common	Com1-5			Load
	External Supply Voltage	V1-5			õ
	Output	Output 6		13	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_07	Common	Com1-6			LOBO
	External Supply Voltage	V1-6			
	Output	Output 7		15	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_08	Common	Com1-7			Load
	External Supply Voltage	V1-7			\frown
	Output	Output 8		17	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_09	Common	Com2-8			Load
	Common Supply Voltage	V2-8			\frown .
	Output	Output 9		19	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_10	Common	Com2-9			LOAD
	Common Supply Voltage	V2-9			\frown .
	Output	Output 10		21	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_11	Common	Com2-10			LOad
	Common Supply Voltage	V2-10			\frown
	Output	Output 11		23	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_12	Common	Com2-11			LOAD
	Common Supply Voltage	V2-11			\frown
	Output	Output 12		25	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_13	Common	Com3-12			Load
	Common Supply Voltage	V3-12			\frown
	Output	Output 13		27	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_14	Common	Com3-13			LOBO
	Common Supply Voltage	V3-13			\frown
	Output	Output 14		29	- (4-20mA) +
MBI.A1.A02.G1.N6_AO_15	Common	Com3-14			LOBO
	Common Supply Voltage	V3-14			\frown
	Output	Output 15		31	- (4-20mA) +
MBI.A1.A02.G1.N6_AO 16	Common	Com3-15			Load
	Common Supply Voltage	V3-15		► To +	24VDC
				► To 2	4COM



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The memory map indicates that there are no read addresses and 64 byte (32 words) write addresses. Each Analog channel uses 4 bytes. The first and second bytes of a channel contain the analog data. The third and fourth bytes are not used at this time. Channel 1 is an exception, where the 4th byte is the Module Control Byte. See Figure 8-30 for the memory map for the Automation Direct T1F-16DA-1 module.

Memo	ry M	ap of	16-0	Cha	nnel	Analo	og O	utput	Module	Decimal Bit	07	06	05	04	03	02	01		00	Cine	
Dealer at Div	07	0.0	(T1F	-016	DA)	04	0.0		Octal Bit	07	06	05	04	03	02	01		00	SIZE	
Decimal Bit	07	06	05	04	03	02	01	00	Size			A	nalog	Valu	ie C	hann	el 9			Write Byte 33	
Octal Bit	07	06	05	04	03	02	01	00				A	nalog	Valu	le C	hann	el 9			Write Byte 34	
		N	ot Us	ed					Read Byte 1					not	useo	1				Byte35	
		Ar	halog	y Val	ue C	nanne	al 1		Write Byte 1			ň	eserv	ed fo	or fut	ure ı	JSe			Byte36	
		Ar	nalog	y Val	ue C	nanne	al 1		Write Byte 2			Analog Value Channel 10							Write Byte 37		
				no	useo	1	-		Byte3		Analog Value Channel 10							Write Byte 38			
	Module Control Byte						Write Byte 4					not	useo	ł				Byte39			
	Analog Value Channel 2						Write Byte 5			ň	eserv	ed fo	or fut	ure ı	JSe			Byte40			
	Analog Value Channel 2					Write Byte 6			Ar	alog	Valu	e Cł	nanne	əl 11			Write Byte 41				
	not used					Byte7			Ar	alog	Valu	e Cł	nanne	əl 11			Write Byte 42				
	reserved for future use					Byte8					not	useo	ł				Byte43				
	Analog Value Channel 3						Write Byte 9			ň	eserv	ed fo	r fut	ure u	JSe			Byte44			
		Ar	nalog	ı Val	ue C	nanne	3 3		Write Byte 10			An	alog	Valu	e Ch	anne	əl 12			Write Byte 45	
				no	used	i			Byte11			An	alog	Valu	e Ch	anne	əl 12			Write Byte 46	
	reserved for future use					Byte12		not used							Byte47						
		Ar	halog	y Val	ue C	nanne	al 4		Write Byte 13			ň	eserv	ed fo	or fut	ure u	JSe			Byte48	
		Ar	nalog	y Val	ue Cl	nanne	al 4		Write Byte 14			An	alog	Valu	e Ch	anne	əl 13			Write Byte 49	
				no	used	i			Byte15	Byte15 Ana						Analog Value Channel 13					
		re	eserv	/ed f	or fut	ure u	se		Byte16			not used							Byte51		
		Ar	nalog	i Val	ue C	nanne	el 5		Write Byte 17		reserved for future use							Byte52			
		Ar	nalog	y Val	ue C	nanne	al 5		Write Byte 18		Analog Value Channel 14						Write Byte 53				
				no	useo	1			Byte19			An	alog	Valu	e Ch	anne	əl 14			Write Byte 54	
		re	eserv	/ed f	or fut	ure u	se		Byte20					not	useo	ł				Byte55	
		Ar	nalog	y Val	ue C	nanne	el 6		Write Byte 21			ň	eserv	ed fo	r fut	ure u	JSe			Byte56	
		Ar	nalog	i Val	ue C	nanne	el 6		Write Byte 22			An	alog	Valu	e Ch	anne	el 15			Write Byte 57	
				no	useo	i			Byte23			An	alog	Valu	e Ch	anne	əl 15			Write Byte 58	
		re	eserv	/ed f	or fut	ure u	se		Byte24					not	used	ł				Byte59	
		Ar	nalog	y Val	ue C	nanne	el 7		Write Byte 25			ň	eserv	ed fo	or fut	ure u	JSe			Byte60	
		Ar	nalog	y Val	ue C	nanne	el 7		Write Byte 26			An	alog	Valu	e Ch	anne	el 16			Write Byte 61	
	not used reserved for future use				Byte27 Analog Value Channel 16 Byte28 not used								Write Byte 62								
												Byte63									
	Analog Value Channel 8				Write Byte 29	reserved for future use							Byte64								
	Analog Value Channel 8				Write Byte 30																
	not used					Byte31															
	reserved for future use					Byte32															

Figure 8-30. T1F-16DA-1 Module Memory Map

Since each channel has two read words, each block address is incremented by two, starting at 40001 and going to 40031. Based on the memory map and the Control Byte Table, there are 32 bits (4 bytes or 2 words) allocated for channel 1. The 4th byte (2nd word) is designated as the Module Control Byte. The control byte allows discrete bits to be set for certain functions. This byte is addressed using word address 40002. Individual bits are set using a B16_TO_A block to write to address 40002. See Figure 8-31 for example of the Control Byte GAP block. The first 8 bits (byte3) are not used. Bit 24 in the Control Byte Table is the IN_9_1 field on the block. This bit is the first bit in byte 4. In this example, Outputs Enable (bit 24) is on (True), the polarity is Unipolar (False), the Voltage Range (bit 26) is not needed and therefore set to (False), the Current Range (bit 27) is set for 0–20 mA (False), and bits 28–31 are reserved and therefore set to (False). See Figure 8-32 for T1F-16DA-1 Control Byte Table.

Module Control Byte of 8&16-Channel Analog Output Module (T1F–08DA, T1F–16DA)											
Decimal Bit	31	30	29	28	27	26	25	24	Pood/M/rito		
Octal Bit	37	36	35	30	Reau/White						
Bit 24		1	Ou 0 = A = All		Write						
Bit 25		C	Unip) = U 1 = E		Write						
Bit 26			5V 0 1	/ 10\ = 5V = 10\	/ Rar / rang / ran	n ge ge ge			Write		
Bit 27		0 – 2	20m/ 0 = 0 1 = 4		Write						
Bit 28 – 31		Re		-							

Figure 8-31	. T1F-16DA-1	Control Byte	Table
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Figure 8-32. T1F-16DA-1 Control Byte GAP Block (example)

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To scale the analog output words for the T1F-16DA-1 module, CURVE_2D blocks were used. The GAP block converts the input, in milliamps, to raw counts used by the T1F-16AD-1 module. The milliamp to raw count relationship can be found in the Automation Direct manual T1K-INST-M. See Figure 8-33 for Current Output Module Resolution. The charts specify counts in decimal form. In this example, the 0–20 mA scaling graph was used. The CURVE_2D block can be scaled to output any engineering units desired. In this example, the CURVE_2D block was scaled to output 0–4095 counts when 0–20 mA is seen by the application. The X_1 field specifies the minimum milliamp value (0 mA) from the GAP application. The X_2 field specifies the maximum milliamp value (20mA) from the GAP application. The Y_1 field specifies the block output in counts (0 counts) when the minimum input is 0 mA. The Y_2 field specifies the block output in counts (4095 counts) when the maximum input is 20 mA. The CURVE_2D X_1 field has been set up as tunable so that the block can be set for a 4–20 mA scale if desired. See Figure 8-34 for example of T1F-16DA-1 GAP CURVE_2D scaling blocks.



Figure 8-33. T1F-16DA-1 Current Output Module Resolution





Automation Direct Thermocouple Input (T1F-14THM) Module



In this example, Type K thermocouples are used. See Figure 8-35 for example of T1F-14THM module wiring.

DI Automat THERM	STRIBUTED I/O ion Direct T1F-14TH OCOUPLE INPUT	łM				
	TO (1)	1 4	Chromel		Chrome	el.
MBI.A1.A02.G1.N7_TC_01		1	Alumel		Alumel	\geq
	TC (+)	2+				_
MBI.A1.A02.G1.N7_TC_02		2		÷		>
	TC (+)	3+		- 5		_
MBI.A1.A02.G1.N7_TC_03	TC (-)	3-				\geq
	TC (+)	4+		<u> </u>		_
MBI.A1.A02.G1.N7_TC_04	TC (-)	4-		╞		\geq
	TC (+)	5+	·	-9-		_
MBI.A1.A02.G1.N7_IC_05	TC (-)	5-		- 10 -		\geq
	TC (+)	6+		- 11 -		~
MBI.A1.A02.G1.N7_1C_06	TC (-)	6-		- 12 -		\rightarrow
	TC (+)	7+		- 13 -		~
MBI.AT.A02.GT.N7_TC_07	TC (-)	7-		- 14 -		
	TC (+)	8+		- 15 -		~
MBI.AT.AUZ.GT.N7_TC_00	TC (-)	8-		- 16 -		
MBLA1 A02 G1 N7 TC 09	TC (+)	9+		- 17 -		~
MDI.AT.A02.01.N7_10_08	TC (-)	9-		- 18 -		_
MBLA1 A02 G1 N7 TC 10	TC (+)	10+		- 19 -		~
MDI.A1.A02.01.N7_10_10	TC (-)	10-		20		_
MBLA1 A02 G1 N7 TC 11	TC (+)	11+		21		~
	TC (-)	11-		22		_
MBLA1.A02.G1.N7 TC 12	TC (+)	12+				
	TC (-)	12-		24		_
MBI.A1.A02.G1.N7_TC_13	TC (+)	13+				\sim
		13-		26		_
MBI.A1.A02.G1.N7_TC_14		14+				\rightarrow
					слс	
		+24V				
	241/ (+)	+24V +24V +24V		C		
	240(1)	+24V +24V +24V				
	24V (-)	+24V 0V 0V 0V 0V 0V 0V		To 24CO!	м	
		0V				

Figure 8-35. T1F-14THM Module Wiring (example)

According to Automation Direct, the T1F-14THM module uses the same memory map as the T1F-16AD-1 module. Based on this memory map, there are 64 byte (32 words) addresses and no write byte addresses. See Figure 8-36 for memory map of Automation Direct T1F-14THM Thermocouple module.

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Mem	Memory Map of 16-Channel Analog Input Module						Module	Decima	al Bit	07	06	05	04	0	3 02	2 0)1	00	Ciao			
Desired Dit	07 0	â	0.5	(11	F-1	6A	D)	04	0.0		Octal	Bit	07	06	05	04	0	3 02	2 0)1	00	3128
Decimal Bit	07 0	0	uo eu	0	<u>, (</u>	33	02	01	00	Size				Ar	halog	Valu	Je (Chanr	nel 9)		Read Byte 33
Octal Bit	07 0	5	05	0	1 0)3	02	01	00					Ar	nalog	Valu	Je (Chanr	nel 9)		Read Byte 34
		Ana	loc	y Va	lue	Cha	anne	11		Read Byte 1						not	use	ed				Byte35
		Ana	lloč	g Va	lue	Cha	anne	11		Read Byte 2				re	serv	ed fo	or fi	uture	use			Byte36
				nc	t us	ed				Byte3			Analog Value Channel 10						Read Byte 37			
		Analog Value Chappel 2			Byte4			Analog Value Channel 10						Read Byte 38								
	Analog Value Channel 2			Read Byte 5						not	use	ed				Byte39						
	Analog Value Channel 2			Read Byte 6				re	serv	ed fo	or fi	uture	use			Byte40						
	not used				Byte7				An	alog	Valu	еC	Chann	el 11	1		Read Byte 41					
		res	ien.	/ed	for f	utu	re us	5e		Byte8				An	alog	Valu	еC	Chann	el 11	1		Read Byte 42
	Analog Value Channel 3 Analog Value Channel 3				Read Byte 9						not	use	ed		_		Byte43					
		Ana	lloč	g Va	lue	Cha	anne	13		Read Byte 10				re	serv	ed fo	or fi	uture	use			Byte44
				nc	t us	ed				Byte11				An	alog	Valu	еC	Chann	el 12	2		Read Byte 45
		res	ier.	/ed	for f	utu	re us	5e		Byte12				An	alog	Valu	еC	Chann	el 12	2		Read Byte 46
	Analog Value Channel 4			Read Byte 13						not	use	ed	_	_		Byte47						
		Ana	lloč	g Va	lue	Cha	anne	4		Read Byte 14			reserved for future use						Byte48			
				nc	t us	ed				Byte15			Analog Value Channel 13						Read Byte 49			
		res	۶er	/ed	for f	utu	re us	5e		Byte16			Analog Value Channel 13						Read Byte 50			
		Ana	ilog	g Va	lue	Cha	anne	15		Read Byte 17						not	use	ed		_		Byte51
		Ana	iloç	g Va	lue	Cha	anne	15		Read Byte 18				re	serv	ed fo	or fi	uture	use	_		Byte52
				nc	t us	ed				Byte19				An	alog	Valu	еC	Chann	el 14	4		Read Byte 53
		res	۶er	/ed	for f	utu	re us	se		Byte20				An	alog	Valu	еC	Chann	el 1/	4		Read Byte 54
		Ana	iloç	g Va	lue	Cha	anne	16		Read Byte 21					-	not	use	ed		_		Byte55
		Ana	iloç	g Va	lue	Cha	anne	16		Read Byte 22				re	serv	ed fo	or fi	uture	use			Byte56
				nc	t us	ed				Byte23				An	alog	Valu	еC	Chann	el 1	5		Read Byte 57
		res	ien	/ed	for f	utu	re us	5e		Byte24				An	alog	Valu	еC	Chann	el 1	5		Read Byte 58
		Ana	iloç	g Va	lue	Cha	anne	17		Read Byte 25						not	use	ed		-		Byte59
		Ana	iloç	g Va	lue	Cha	anne	17		Read Byte 26				re	serv	ed fo	or fi	uture	use	_		Byte60
				nc	t us	ed				Byte27				An	alog	Valu	еC	Chann	el 16	8		Read Byte 61
	reserved for future use			Byte28			Analog Value Channel 16						Read Byte 62									
	Analog Value Channel 8			Read Byte 29			not used						Byte63									
	Analog Value Channel 8				Read Byte 30			reserved for future use						Byte64								
	not used					Byte31			Not Used						Write Byte 1							
		res	;er	/ed	for f	utu	re us	5e		Byte32				-								

Figure 8-36. T1F-14THM Thermocouple Module Memory Map

The T1F-14THM Thermocouple module has a set of jumpers located under the top cover of the module for configuring certain parameters. See Figure 8-37 for T1F-14THM jumper configuration tables. In this example, the jumpers were configured for 14 channels of Type K thermocouples with output in degrees F and calibration disabled. See Figure 8-38 for T1F-14THM jumper locations.

T/C Type 3

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Thermocounle	Conversion	Unite
rnermocoupie	Conversion	Units

	Temperature Conversion Units									
Jumper	Magnitu Si °F	ide Plus gn °C	2's Com °F	∘C						
Units-0	х		Х							
Units-1	Х	х								

X = Jumper Installed,

Blank Space = Jumper Removed

X = Jumper Installed, Blank Space = Jumper Removed

Figure 8-37. for T1F-14THM Jumper configuration Tables





Figure 8-38. T1F-14THM Jumper Locations

It has been found that the thermocouple value must be scaled to one tenth the value output by the T1K-MODBUS interface module. A divide by 10 block is used to scale the thermocouple inputs. See Figure 8-39 for example of T1F-14THM GAP DIVIDE Blocks.





Chapter 9. Communications

Modbus Communication

The AtlasSC[™] control can communicate with plant distributed control systems (DCS) and/or CRT-based operator control panels through three Modbus® communication ports. These ports support RS-232, RS-422, or RS-485 communications using ASCII or RTU MODBUS transmission protocols. Modbus uses a master/slave protocol. This protocol determines how a communication network's master and slave devices establish and break contact, how a sender is identified, how messages are exchanged, and how errors are detected.

The AtlasSC control supports two Modbus transmission modes. A mode defines the individual units of information within a message and the numbering system used to transmit the data. Only one mode per Modbus network is allowed. The supported modes are ASCII (American Standard Code for Information Interchange), and RTU (Remote Terminal Unit). These modes are defined in the following table.

CHARACTERISTIC	ASCII	RTU			
	hexadecimal (uses ASCII	8-bit binary			
Coding System	printable binary characters:				
	0-9, A-F)				
Start Bits	1	1			
Data Bits per Char	7	8			
Parity	even, odd, or none	even, odd, or none			
Stop Bits	1, 1.5, or 2	1, 1.5, or 2			
	110, 300, 600, 1200, 1800,2400,	110,300, 600, 1200, 1800, 2400,			
Baud Rate	4800, 9600, 19200, 38400, or	4800, 9600, 19200, 38400, or			
	57600	57600			
Error Chocking	LRC (Longitudinal	CRC (Cyclical			
Endr Checking	Redundancy Check)	Redundancy Check)			

Table 9-1. ASCII vs RTU Modbus

In the RTU mode, data is sent in 8-bit binary characters and transmitted in a continuous stream. In the ASCII mode, each binary character is divided into two 4-bit parts (high order and low order), changed to be represented by a hexadecimal equivalent, then transmitted, with breaks of up to 1 second possible. Because of these differences, data transmission with the ASCII mode is typically slower (see Figure 9-1).



Figure 9-1. ASCII/RTU Representation of 3

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The Modbus protocol allows one master and up to 247 slaves on a common network. Each slave is assigned a fixed, unique device address in the range of 1 to 247. With the Modbus protocol, only the network master can initiate a transaction. A transaction consists of a request from the master to a slave unit and the slave's response.

The AtlasSC control can directly communicate with a DCS or other Modbus supporting device on a single communications link, or through a multi-dropped network. If multi-dropping is used, up to 246 devices (AtlasSC controls or other customer devices) can be connected to one Master device on a single network.

Each message to or from a master has a defined structure called the message "frame". A frame consists of the slave device address, a code defining the requested data, and error checking information (see Figure 9-2).

	BEGINNING OF FRAME	SLAVE ADDRESS	FUNCTION CODE	DATA	ERROR CHECK CODE	END OF FRAME
ASCII	÷	2 CHARS 8 BITS	2 CHARS 8 BITS	4 BITS DATA PER CHAR	2 CHAR 8 BITS	CR LF
RTU	3-CHAR DEAD TIME	1 CHAR 8 BITS	1 CHAR 8 BITS	8 BITS DATA PER CHAR	2 CHAR 16 BITS	3 CHAR DEAD TIME
	L					

Table 9-2. Modbus Frame Definition

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The Modbus function code tells the addressed slaves what function to perform. The following table lists the function codes supported by this control.

Modbus Function Codes

Table 9-2. Modbus Function Codes

CODE	DEFINITION	REFERENCE ADDRESS
01	Read Digital Outputs (Raise/Lower and Enable/Disable Commands)	0XXXX
02	Read Digital Inputs (Status Indications / Alarms and Trips)	1XXXX
03	Read Analog Outputs	4XXXX
04	Read Analog Inputs (Speed, Setpt, etc)	3XXXX
05	Write Single Discrete Output (Raise/Lower and Enable/Disable Commands)	0XXXX
06	Write Single Register (Enter Setpt Directly)	4XXXX
08	Loopback Diagnostic Test (supports subfunction 0 only)	N/A
15	Write Digital Outputs	0XXXX
16	Write Analog Outputs	4XXXX

When a Modbus message is received, it is checked for any errors or invalid data. If there is invalid data in the message, an error code is sent back to the master and the control issues an alarm message. The error codes are defined in the following table.

Table 9-3. Modbus Slave Exception Error Codes

CODE	ERROR MESSAGE	TO MASTER	DESCRIPTION
0	No Error	0	No Error
1	Bad Modbus function	1	The specified function is not supported for this control
2	Bad Modbus data address	2	The Modbus value address is not valid for this control
3	Bad Modbus data value	3	Too many values requested or the on/off indicator in function code 5 is invalid
9	Bad Modbus checksum	None	Message checksum did not match
10	Bad Modbus message	None	Message could not be decoded
n/a	Lost Modbus link	None	No messages received for the configured time- out period

Port Adjustments

Before the AtlasSC control will communicate with another device, the communication parameters must be verified. These values are set in the GAP program, and the GAP programmer may also make these values tunable if desired.

Table 9-4. Modbus Communication Port Adjustments

PARAMETER	ADJUSTMENT RANGE
Baud Rate	110 TO 57600
Parity	NONE, ODD, or EVEN
Stop Bits	1 TO 2
Driver	RS-232, RS-422, or RS-485

Chapter 10. Programming and Service Tools

Introduction

The primary program download and service tool for the AtlasSC[™] control is the Woodward Watch Window system. This software runs on a PC that is connected to the control with an RS-232 serial cable. Also running on the same PC is the Woodward ServLink I/O server software that allows Watch Window to access selected application variables that were generated in the GAP[™] application program. The details of the specific GAP application programming are beyond the scope of this manual, but Woodward publication number 80018 is available to assist the application engineer in this process.

Two versions of Watch Window are available for use with the AtlasSC control:

- 1. Watch Window Professional is a licensed software tool that enables the GAP-generated application program to be downloaded into the AtlasSC hardware. From that point on, Watch Window Professional is used to
 - Configure the control software to the specific site or application needs
 - Monitor and tune system variables that were selected in the GAP application program
 - Upload and download all tunable and configuration variables from the AtlasSC control
- 2. Watch Window Standard is a software tool that does not have the capability to download application software. Otherwise, it provides the same functionality as Watch Window Professional

This chapter covers the following procedures:

- Connecting the AtlasSC control to a PC
- Loading the Watch Window Professional or Watch Window Standard software as well as the ServLink I/O software on the PC
- Applying power to the control
- Opening communications with the control
- Downloading the GAP-generated application software to the AtlasSC control
- Using Watch Window to view variables and to tune the control
- Uploading and downloading tunable and configuration variables from/to the AtlasSC control

Connecting the AtlasSC Control to a PC

Connection of a generic PC to the AtlasSC control is required in order to load application software and view/tune within that software application. Figure 10-1 shows the connection details.



Figure 10-1. Connecting the AtlasSC Control to a PC

Loading Watch Window and ServLink on the PC

Woodward's Watch Window Standard and Watch Window Professional tools may be downloaded from the Woodward website (**www.woodward.com/ic**). Alternatively, a Watch Window CD Install Kit may be purchased from your Woodward distributor. Once downloaded, the kit's Setup.exe program will guide the user through the installation of Watch Window and ServLink software on the PC hard drive.

Applying Power to the AtlasSC Control

At power-up, the AtlasSC control runs through its boot-up routine and performs a set of initial diagnostics to verify CPU, memory, and bus health. This boot-up routine takes up to 90 seconds to execute. During this time, the control's red status LED (located at the bottom/center area of the control) should be illuminated. When boot-up is complete, the application program (if loaded) will begin running, the control's outputs will be enabled, and system control will begin. The red status LED will be turned off and should remain off as long as the control is running.

Opening Communications with the AtlasSC Control

Initial AtlasSC Communications

Before communications can begin between the Watch Window software and the AtlasSC control, a Network Definition file must be created. Once this file is created and saved, it never has to be recreated unless a new GAP application program is installed.

Create the Network Definition File

- 1. Make sure that all other programs that may access your computer's communication port are shut down.
- Start the ServLink software by selecting Start> Programs> Woodward> Watch Window Professional (or Watch Window Standard)> ServLink Server on the PC. Then select File> New on the ServLink dialog window. In the dialog window (shown below) select the proper COM port for your computer, select POINT TO POINT communications mode, and set the baud rate to 57600. This is the factory default baud rate. If the control has been

re-programmed after leaving the factory, the user may need to contact the entity that supplied the new program to determine the correct baud rate.

IMPORTANT If a previous download activity was interrupted before completion of the download, the control will have no installed program. In this instance, after the control is reset and the boot-up period has elapsed, the control will continuously scan COM3 port at 57600 and 115200 baud looking for a connection to ServLink. The user may have to hit <Retry> a few times until the control and ServLink baud rates are matched and communications are established.

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Network Options		×
Use this port Port: Communications Port (COM1)	From this location Location: New Location	Cancel <u>H</u> elp
In this mode Mode: Point-to-Point At this baud rate Baud Rate: 57600	Using this phone number <u>C</u> ountry Code: United States of America (1) <u>Area Code:</u> 970 <u>Phone Number:</u> Number Being Dialed:	

3. Select OK. If everything is working correctly there will be an animated graphic of a string of "1"s and "0's flying from the control to the PC on the screen. Once this transfer is completed, a Network Definition file whose default name is NET1.NET will have been generated. This file should be saved by using the 'File> Save As' feature on the PC. Link this name to the control part number as it will only work with that application. For example, if the control part number is 8273-040, one can save the Network Definition file as 8273040.NET. See example below. Do not disconnect the server.

Save As						? ×
Savejn: 🔂	Servlink Server	•	← 🔁	e ř	•	
I						
File <u>n</u> ame:	8273040.NET				<u>S</u> ave	,
Save as <u>t</u> ype:	Network Definition Files (*.net)		•		Cance	

4. In the ServLink window there will be another dialog window entitled "your filename.NET". Unless the user has given the control a serial number (or name) with the SLSN.EXE program, this name will display as "<undentified>". The user should note this name. See example below.

📌 ServLink I/O Server - 8273040.NET	_ 🗆 🗵
File Edit View Options Tools Window Help	
₩8273040.NET	
🕀 🚓 Dflt Control ID1	
1	
For Help, press F1	

Downloading the GAP Application Code

The AtlasSC control is shipped from the factory with a default GAP program. In order to download a new GAP-generated application program it is necessary to use the Watch Window Professional service tool. The following steps are required for this program downloading process.

Download the Application File

 Start the Watch Window program by selecting Start> Programs> Woodward> Watch Window Professional> Watch Window Professional on the PC. Under the title bar in the Explorer section of the screen, a tab with the Network Definition filename and the Control ID will be displayed. Right click on this tab to display a pop-up menu and select LOAD APPLICATION. See example screen below.

My Documents WISE		💂 📶 Offic
Wy Cont Ow Watch Window Professional File Edit Sheet Control Options Window Help Image: Sheet Image: Sheet Control Options Image: Sheet		e 🎢 🗐
My Net Explorer Impector Place 8273040 NET(Dit Control (D1) Losd Application		
Internet A01_TC Save Values Internet A1_A01_S10 Properties		Roll I
Image: Comparty Home P Or -		
Noros Outo		
I repip shortcut to Request GTC.xox PassConnect Wondward		ų
第5tart 11 (2) @ 図 図 100 Inbox - Microso 1 (3) Manuals チタServLink I/O Se 1 (30) Watchwindow (22) N しょち	⊗ ≈ 0%⊒≞ ∘:	Aicrosoft 18 AM

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2. Once the Load Application selection has been made, the Inspector window will close and a new window will appear. In this new window the user enters the name of the file that is to be downloaded into the control. Once the filename is correct, click on the Open button. See example below

My Docu	nents WISE				
My Com	Watch Window Professional File Edit Sheet Control Options W	indow Help		_	ffice 🗐 🗐
My Netr Place Recycle	Image: Control (Control (Control (CO))) B273040.NET(Dift: Control (CO)) Image: Control (CO) Image: Control (CO) </th <th>Look in: GTC xxx B 8272040.5CP</th> <th>* \$\$</th> <th>?× * ∎•</th> <th></th>	Look in: GTC xxx B 8272040.5CP	* \$\$?× * ∎•	
Interr Exploi Departr Home F		File name: 8273040.SCP Files of type: Application Script File	ts (*.scp) 💌	Open Cancel	
Micros Outlo IT He Reque	p Shartout to st GTC xxx				
PassCor Woodwa	net. rd	rrooft Out	🔶 Sand ink 1/0 San or -	1 C2 Watchwindows	Microsoft

- 3. A warning screen will ask that the user make sure the prime mover is shut down before downloading. When the warning screens have been addressed, the downloading will commence automatically. This may take several minutes depending on the size of the application. When downloading is complete, the control will reboot automatically and the red status LED will come on. When reboot is completed, the red status LED will go out and the control will begin execution of the new application program.
- 4. At the end of the download process, an Information window will appear in Watch Window (see below) to alert the user to the fact that a new Network Definition file must be created that matches the new program that has been downloaded. The same procedure that was used initially to create a Network Definition file can be used again.



unit is running during the download procedure.

Using Watch Window

Here are brief instructions for using the Watch Window in conjunction with ServLink to view and tune the variables for your AtlasSC control. These instructions are meant to be introductory only. Full on-line help is available in each application. It is assumed that you already have ServLink and Watch Window installed. The default installation location can be found by clicking the START icon (Microsoft Windows 95 or later) on the main menu bar and then clicking the PROGRAMS menu item. Look for an icon called WOODWARD WATCH WINDOW.

- 1. Make sure that all other programs that may access your computer communications port are shut down.
- Start the ServLink server and open a new file. Select the proper COM port for your PC, verify that POINT TO POINT communications mode is selected, and verify that the baud rate matches the baud rate of the AtlasSC control. See the on-line help file if you have been changing the port settings of your control.
- 3. Select OK. If everything is working right, you should see an animated picture of a string of "1"s and "0"s flying from the control to the PC on your screen. You now have a network definition file whose default name is NET1. You should save this file as "your filename.net" (use FILE/SAVE AS). Link this name to your control part number, as it will work only with that application. For instance, if the upper level control number is 8273-040, you could save the file as 8273040.NET. Do not disconnect the server.
- 4. In the ServLink window you will now have another dialog window titled "your filename.net". In this window you will see a ballhead icon and a control identifier name. Unless you have given the control a serial number (or name) with the SLSN.EXE program, this name will display as "<unidentified>".
- 5. Start the Watch Window application. When Watch executes, you will have a screen displaying three windows entitled Watch Window, Explorer, and Inspector.
- 6. The Explorer window will have two groups displayed, SERVICE and CONFIGURE. Double clicking on either of these will expand them to show groups of values. Explorer is used only to locate a tunable or monitor value. In order to change a value or monitor a value, you must drag and drop a value from the Explorer window into the Inspector window.
- 7. Once a value is displayed in the Inspector, you can see several blocks of information. The most important blocks for a tunable value are the FIELD and VALUE blocks. The FIELD block is used to identify a particular value, and the VALUE block displays the current value of a variable. There are two types of values available in Watch. One is a monitor value, which is marked in the INSPECTOR window with a pair of glasses. This means it may only be looked at. The other value is a read/write value, which is marked with a pencil. The read/write type may be modified using the up and down arrows in the value block.

Chapter 11. Product Support and Service Options

Product Support Options

If you are experiencing problems with the installation, or unsatisfactory performance of a Woodward product, the following options are available:

- Consult the troubleshooting guide in the manual.
- Contact the manufacturer or packager of your system.
- Contact the Woodward Full Service Distributor serving your area.
- Contact Woodward technical assistance (see "How to Contact Woodward" later in this chapter) and discuss your problem. In many cases, your problem can be resolved over the phone. If not, you can select which course of action to pursue based on the available services listed in this chapter.

OEM or Packager Support: Many Woodward controls and control devices are installed into the equipment system and programmed by an Original Equipment Manufacturer (OEM) or Equipment Packager at their factory. In some cases, the programming is password-protected by the OEM or packager, and they are the best source for product service and support. Warranty service for Woodward products shipped with an equipment system should also be handled through the OEM or Packager. Please review your equipment system documentation for details.

Woodward Business Partner Support: Woodward works with and supports a global network of independent business partners whose mission is to serve the users of Woodward controls, as described here:

- A **Full Service Distributor** has the primary responsibility for sales, service, system integration solutions, technical desk support, and aftermarket marketing of standard Woodward products within a specific geographic area and market segment.
- An Authorized Independent Service Facility (AISF) provides authorized service that includes repairs, repair parts, and warranty service on Woodward's behalf. Service (not new unit sales) is an AISF's primary mission.
- A **Recognized Turbine Retrofitter (RTR)** is an independent company that does both steam and gas turbine control retrofits and upgrades globally, and can provide the full line of Woodward systems and components for the retrofits and overhauls, long term service contracts, emergency repairs, etc.

A current list of Woodward Business Partners is available at www.woodward.com/directory.

Product Service Options

The following factory options for servicing Woodward products are available through your local Full-Service Distributor or the OEM or Packager of the equipment system, based on the standard Woodward Product and Service Warranty (5-01-1205) that is in effect at the time the product is originally shipped from Woodward or a service is performed:

- Replacement/Exchange (24-hour service)
- Flat Rate Repair
- Flat Rate Remanufacture

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Replacement/Exchange: Replacement/Exchange is a premium program designed for the user who is in need of immediate service. It allows you to request and receive a like-new replacement unit in minimum time (usually within 24 hours of the request), providing a suitable unit is available at the time of the request, thereby minimizing costly downtime. This is a flat-rate program and includes the full standard Woodward product warranty (Woodward Product and Service Warranty 5-01-1205).

This option allows you to call your Full-Service Distributor in the event of an unexpected outage, or in advance of a scheduled outage, to request a replacement control unit. If the unit is available at the time of the call, it can usually be shipped out within 24 hours. You replace your field control unit with the like-new replacement and return the field unit to the Full-Service Distributor.

Charges for the Replacement/Exchange service are based on a flat rate plus shipping expenses. You are invoiced the flat rate replacement/exchange charge plus a core charge at the time the replacement unit is shipped. If the core (field unit) is returned within 60 days, a credit for the core charge will be issued.

Flat Rate Repair: Flat Rate Repair is available for the majority of standard products in the field. This program offers you repair service for your products with the advantage of knowing in advance what the cost will be. All repair work carries the standard Woodward service warranty (Woodward Product and Service Warranty 5-01-1205) on replaced parts and labor.

Flat Rate Remanufacture: Flat Rate Remanufacture is very similar to the Flat Rate Repair option with the exception that the unit will be returned to you in "like-new" condition and carry with it the full standard Woodward product warranty (Woodward Product and Service Warranty 5-01-1205). This option is applicable to mechanical products only.

Returning Equipment for Repair

If a control (or any part of an electronic control) is to be returned for repair, please contact your Full-Service Distributor in advance to obtain Return Authorization and shipping instructions.

When shipping the item(s), attach a tag with the following information:

- Return authorization number
- Name and location where the control is installed
- Name and phone number of contact person
- Complete Woodward part number(s) and serial number(s)
- Description of the problem
- Instructions describing the desired type of repair

Packing a Control

Use the following materials when returning a complete control:

- Protective caps on any connectors
- Antistatic protective bags on all electronic modules
- Packing materials that will not damage the surface of the unit
- At least 100 mm (4 inches) of tightly packed, industry-approved packing material
- A packing carton with double walls
- A strong tape around the outside of the carton for increased strength



To prevent damage to electronic components caused by improper handling, read and observe the precautions in Woodward manual 82715, *Guide for Handling and Protection of Electronic Controls, Printed Circuit Boards, and Modules.*

Replacement Parts

When ordering replacement parts for controls, include the following information:

- The part number(s) (XXXX-XXXX) that is on the enclosure nameplate
- The unit serial number, which is also on the nameplate

Engineering Services

Woodward offers various Engineering Services for our products. For these services, you can contact us by telephone, by email, or through the Woodward website.

- **Technical Support** •
- Product Training
- Field Service

Technical Support is available from your equipment system supplier, your local Full-Service Distributor, or from many of Woodward's worldwide locations, depending upon the product and application. This service can assist you with technical questions or problem solving during the normal business hours of the Woodward location you contact. Emergency assistance is also available during non-business hours by phoning Woodward and stating the urgency of your problem.

Product Training is available as standard classes at many of our worldwide locations. We also offer customized classes, which can be tailored to your needs and can be held at one of our locations or at your site. This training, conducted by experienced personnel, will assure that you will be able to maintain system reliability and availability.

Field Service engineering on-site support is available, depending on the product and location, from many of our worldwide locations or from one of our Full-Service Distributors. The field engineers are experienced both on Woodward products as well as on much of the non-Woodward equipment with which our products interface.

For information on these services, please contact us via telephone, email us, or use our website: www.woodward.com.

Contacting Woodward's Support Organization

For the name of your nearest Woodward Full-Service Distributor or service facility, please consult our worldwide directory at www.woodward.com/directory, which also contains the most current product support and contact information.

You can also contact the Woodward Customer Service Department at one of the following Woodward facilities to obtain the address and phone number of the nearest facility at which you can obtain information and service.

Products Used in	Products Used in	Products Used in Industrial
Electrical Power Systems	Engine Systems	Turbomachinery Systems
Facility Phone Number	FacilityPhone Number	Facility Phone Number
Brazil+55 (19) 3708 4800	Brazil +55 (19) 3708 4800	Brazil +55 (19) 3708 4800
China +86 (512) 6762 6727	China +86 (512) 6762 6727	China +86 (512) 6762 6727
Germany:	Germany +49 (711) 78954-510	India+91 (124) 4399500
Kempen +49 (0) 21 52 14 51	India+91 (124) 4399500	Japan+81 (43) 213-2191
Stuttgart - +49 (711) 78954-510	Japan+81 (43) 213-2191	Korea+82 (51) 636-7080
India+91 (124) 4399500	Korea+82 (51) 636-7080	The Netherlands+31 (23) 5661111
Japan+81 (43) 213-2191	The Netherlands+31 (23) 5661111	Poland+48 12 295 13 00
Korea+82 (51) 636-7080	United States+1 (970) 482-5811	United States+1 (970) 482-5811
Poland+48 12 295 13 00		
United States+1 (970) 482-5811		

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Technical Assistance

If you need to contact technical assistance, you will need to provide the following information. Please write it down here before contacting the Engine OEM, the Packager, a Woodward Business Partner, or the Woodward factory:

General	
Your Name	
Site Location	
Phone Number	
Fax Number	
Prime Mover Information	
Manufacturer	
Turbine Model Number	
Type of Fuel (gas, steam, etc.)	
Power Output Rating	
Application (power generation, marine, etc.)	
Control/Governor Information	
Control/Governor #1	
Woodward Part Number & Rev. Letter	
Control Description or Governor Type	
Serial Number	
Control/Governor #2	
Woodward Part Number & Rev. Letter	
Control Description or Governor Type	
Serial Number	
Control/Governor #3	
Woodward Part Number & Rev. Letter	
Control Description or Governor Type	
Serial Number	
Symptoms	
Description	

If you have an electronic or programmable control, please have the adjustment setting positions or the menu settings written down and with you at the time of the call.

Appendix A. Acronyms and Glossary of Terms

Acronyms

- ADC Analog-to-Digital Converter
- AWG American Wire Gauge (metric equivalent is mm²)
- CE The CE marking is a European proof of conformity and is also described as "passport" that allows manufacturers and exporters to circulate products freely within the EU. The letters "CE" (French for "Conformité Européenne") indicate that the manufacturer has satisfied all assessment procedures specified by law for its product.
- CPU Central Processing Unit. Executes the GAP application program.
- CT Current Transformer. Used to measure the generator or bus current.
- dc Direct Current
- EEPROM Electrically Erasable and Programmable Read Only Memory
 - EMC Electromagnetic Conformity
 - EMI Electromagnetic Interference
 - GAP Graphical Application Program
 - I/O Input/Output
 - LED Light Emitting Diode
 - LON Local Operating Network
 - MFT Minor Frame Timer. Used by the CPU for scheduling execution of the software.
 - MPU Magnetic Pick-Up
 - MTBF Mean Time Between Failures
 - PC Personal Computer
 - PCB Printed Circuit Board
 - PT Potential Transformer. Used to measure the generator or bus voltage.
 - PWM Pulse Width Modulated
 - RAM Random Access Memory
 - RG Rate Group. Defines how often software is executed.
 - RTD Resistance Temperature Device
 - RXD Receive Data Line
 - SRAM Static Random Access Memory
 - SSTP Shielded-Shielded Twisted Pair (or Double Shielded Ethernet Cables)
 - THD Total Harmonic Distortion
 - TXD Transmit Data Line
 - V/I Voltage-to-Current converter

Glossary of Terms

AtlasSC[™] Analog Combo board—An AtlasSC board with an analog format that connects to the AtlasSC SmartCore board through the PC/104 interface. It contains 15 analog inputs, 2 speed sensor inputs, and 2 Analog outputs.

AtlasSC Power Supply board—An AtlasSC board with primary power supply and 12 discrete outputs.

AtlasSC SmartCore board—An AtlasSC board with 6 analog inputs, 2 actuator outputs, 6 analog outputs, 2 speed sensor inputs, 24 discrete inputs, and 3 serial communication ports. This board includes both the PC/104 and the power bus connections, allowing it to function as a backplane.
AtlasSC Chassis—A combination of pieces required to hold the boards together.

AtlasSC Platform—The combination of boards, a power supply, and a chassis, that can be combined to compose a variety of controls for a variety of applications. The boards must have either a PC/104 connection, or a proprietary power bus connection, and meet certain packaging constraints.

Analog Input—A 4–20 mA or 0–5 V input on the SmartCore board, and a thermocouple, RTD or 4–20 mA input on the Analog Combo board.

Analog Output—A 4–20 mA output, usually the full range is 0–24 mA.

- **Backplane**—A board that ties other boards together electrically. AtlasSC needs the SmartCore board to connect the power bus voltages to the PC/104 bus.
- **Discrete Input**—An input used for switches or other contacts, that registers only two states, open or closed.

Discrete Output—Output drivers used for driving relays that register only two states, high or low. **Serial Port**—A connection for RS-232, RS-422, or RS-485.

Speed Sensor Input—An MPU or proximity probe input on the SmartCore board, and a MPU only input on the Analog Combo board.

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Appendix B. Wiring Diagrams



Figure B-1. SmartCore Board with Actuators Connections



AtlasSC[™] Digital Control



Figure B-3. Power Supply Board Connections

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Figure B-4. PowerSense Board Connections

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Manual 26179



Figure B-5. 12-Channel Relay Module Connections

Appendix C. **Flash Codes**

Number of LED Flashes

Failure

- CPU Failure (Clock Fail, Invalid Reset, Halt, etc.)
- 2 Unexpected CPU Exception (Bus Error, Address Error, Trap, etc.)
- 3 RAM Error
- 4 Watchdog Timeout
- 5 EEPROM Error
- 6 FLASH Memory Error
- 7 Operating System Error
- 8 Stack Memory Overflow
- 9 Application Checksum Error
- 10 Communication Error (68302 Fail)
- 11 Real Time Clock Error
- 12 Rate Group Slip Error
- 13–15 Unused
 - Data Log Error 16
 - 17 NV Log Error
 - 18 Math Exception

Table C-1. SmartCore Failure Codes

Number of LED Flashes Failure

- Microprocessor failure 1
- 2 Bus, address, any unexpected exception error
- Failure during EE test or erasing 5
- 7 Kernel software Watchdog count error
- 12 Failure during CPU Internal RAM test
- 13 Dual port RAM error

Table C-2. Analog Combo Failure Codes

Number of LED Flashes Failure

No failure, system OK

- Off Solid Module in initialization mode
 - Hardware watchdog, CPU clock failure, reset fail 1
 - Unexpected Exception Error 2
 - 3 RAM test failure
 - 5 **EEPROM** failure
 - 7 Kernel Watchdog Timeout
 - 10 System Error
 - 11 Board Identification Error
 - 12 **TPU RAM failure**
 - 13 Dual Port RAM test failure
 - 14 QSM or ADC Initialization failure
 - 15 Self test status failure
 - 20 Invalid A/D converter selected
 - **QSPI** timeout 21
 - 24 ADC auto calibration time-out

Table C-3. PowerSense Failure Codes



Revision History

Changes in Revision J—

- Removed reference to Zone 2 and DEMKO certification
- Removed EMC, Low Voltage Directive, ATEX and DEMKO references
- Removed European Zone 2 information
- Removed Zone 2, Group IIC information
- Deleted DoC



We appreciate your comments about the content of our publications. Send comments to: <u>icinfo@woodward.com</u>

Please reference publication **26179**.





PO Box 1519, Fort Collins CO 80522-1519, USA 1041 Woodward Way, Fort Collins CO 80524, USA Phone +1 (970) 482-5811

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